





SAT/SMT Solving and Applications in Rewriting

René Thiemann ¹ Sarah Winkler ²

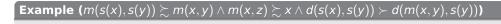
Exercise: Develop an optimized encoder for polynomial constraints

- start from initial polynomial constraints as before
- perform several simplification steps and identify $f_i > 1$ conditions
- eliminate common terms
- eliminate p > 0
- simplify 0 > q to q = 0
- simplify p + q = 0 to p = 0 and q = 0
- simplify $f_i p = 0$ to p = 0 whenever $f_i > 1$ is known
- whenever $f_i p > q$ conclude $f_i > 1$
- whenever $f_i = 0$ is present, substitute and simplify everywhere else
- simplify $f_i p \ge f_i q$ to $p \ge q$ (and also with >) whenever $f_i \ge 1$ is known

Outline

- 1. Solution of Exercise of Session 3
- 2. Beyond Reduction Orders and Termination
- 3. Logically Constrained Term Rewrite Systems
- 4. Further Reading

universität unibz ISR 2024 SAT/SMT Solving and Applications in Rewriting session 4



¹University of Innsbruck

²Free University of Bolzano

Example $(m(s(x),s(y)) \succsim m(x,y) \land m(x,z) \succsim x \land d(s(x),s(y)) \succ d(m(x,y),s(y)))$		
	$m_1s_1\geq m_1$	$m_2s_1\geq m_2$
	$m_1 \geq 1 \\$	
$d_1s_0 > d_1m_0$	$d_1s_1 \geq d_1m_1$	$0 \geq d_1 m_2$
$d_1 \geq 1$	$s_1 \geq 1$	$m_2s_1\geqm_2$
	$m_1 \geq 1 \\$	
$s_0 > m_0$	$s_1 \geq m_1$	$0 \geq m_2$
$d_1 \geq 1$	$s_1 \geq 1$	
	$m_1 \geq 1 \\$	
$s_0 > m_0$	$s_1 \geq m_1$	$m_2 = 0$
final constraints are linear!		
unibz ISR 2024 SAT/SMT Solving and Applications in Rewriting session 4 1. Solution of Exercise of Session 3		

Outline

- 1. Solution of Exercise of Session 3
- 2. Beyond Reduction Orders and Termination
- 3. Logically Constrained Term Rewrite Systems
- 4. Further Reading

Final Exercise

- knowledge: basics to encode KBO, LPO, polynomial order (POLO) via SAT/SMT
- weighted path order (WPO) combines features of KBO, LPO, and POLO
- task 1: lookup definition of WPO and design encoding
- fact: using only WPO, newcomer termination tool NaTT got 2nd place in termComp
- task 2 (optionally ²)
 - implement the encoding in your tool
 - implement dependency pairs and usable rules
 - add CPF generation
 - participate in termComp
- remark: miniTT was implemented from scratch in two days, including library search



universität unibz ISR 2024 SAT/SMT Solving and Applications in Rewriting session 4

Current State

- **1** synthesize reduction orders (KBO, LPO, POLO with $f_i \ge 1$)
- 2 synthesize reduction pairs (KBO + AF, LPO + AF, POLO with $f_i \ge 0$)

Using Reduction Orders

- ensure termination by demanding $\ell_i > r_i$ for all n rules
- ensure incremental termination: rule removal
 - add n Boolean variables strict;
 - \(\strict_i \)
 - $\bigwedge \ell_i \succeq r_i$
 - \bigwedge (strict_i $\rightarrow \ell_i \succ r_i$)
 - solve all of these constraints
 - · afterwards, remove all strictly oriented rules and continue with remaining rules

works good with POLO, and for KBO and LPO with quasi-precedences

Using Reduction Pairs

- main difference to reduction order:

 is closed under contexts, not
- applications for termination
- combine with dependency pairs, use DP framework \Longrightarrow big increase of power
- application for confluence
 - disprove confluence of \mathcal{R} by proving non-joinability
 - given a peak $t_1 \underset{\mathcal{R}}{\sim} \leftarrow s \rightarrow_{\mathcal{R}}^* t_2$, try to prove that join $t_1 \rightarrow_{\mathcal{R}}^* u \underset{\mathcal{R}}{\sim} \leftarrow t_2$ is not possible
 - approach via reduction pairs (or more relaxed: discrimination pairs)
 - for i = 1, 2 approximate usable rules \mathcal{U}_i for t_i such that $t_i \to_{\mathcal{R}}^* v$ implies $t_i \to_{\mathcal{U}_i}^* v$ for all v
 - find reduction pair such that $\mathcal{U}_1 \subseteq \{\succeq\}$ and $\mathcal{U}_2^{-1} \subseteq \{\succeq\}$ and $t_2 \succ t_1$
- application for infeasibility: given s, t, \mathcal{R} , prove that $s\sigma \to_{\mathcal{R}}^* t\sigma$ is not possible
 - approach via reduction pairs (or more relaxed: co-rewrite pairs)
 - find reduction pair such that $\mathcal{R} \subseteq \succeq$ and $t \succ s$ or $\mathcal{R}^{-1} \subseteq \succeq$ and $s \succ t$

ISR 2024 SAT/SMT Solving and Applications in Rewriting session 4

Logically Constrained Rewriting = Term Rewriting + SMT

typically decidable by SMT solvers

Idea

incorporate background theories into rewrite formalism for more convenient/efficient modeling

Terms are built from

• theory symbols \mathcal{F}_{l} , including booleans and =

fixed interpretation in theory

• proper symbols $\mathcal{F}_{\mathcal{T}}$

free

Logically constrained rewrite rules





- left-hand side is rooted by proper symbol
- side constraint is theory term of sort bool
- set of such rules is logically constrained rewrite system (LCTRS)

Outline

- 1. Solution of Exercise of Session 3
- 2. Beyond Reduction Orders and Termination
- 3. Logically Constrained Term Rewrite Systems

4. Further Reading

universität unibz ISR 2024 SAT/SMT Solving and Applications in Rewriting session 4

3. Logically Constrained Term Rewrite Systems

Rewrite relation is union of ...

 $\rightarrow_{\mathsf{rule}}$ $\rightarrow_{\mathsf{calc}}$

apply rewrite rule if substituted constraint valid evaluate theory expressions

Example

LCTRS \mathcal{R} over theory of integer arithmetic:

$$fact(x) \rightarrow 1 \quad [x \leqslant 0]$$

$$fact(x) \rightarrow fact(x-1) \cdot x \quad [x-1 \geqslant 0]$$

admits following rewrite steps:

$$fact(2) \rightarrow_{rule} fact(2-1) \cdot 2$$

$$\rightarrow_{calc} fact(1) \cdot 2$$

$$2-1\geqslant 0$$
 valid

$$\rightarrow_{\mathsf{rule}} (\mathsf{fact}(\,1-1\,)\cdot 1)\cdot 2$$

$$1-1\geqslant 0$$
 valid

$$\to_{\text{calc}} (\,\text{fact}(0)\,\cdot 1)\cdot 2$$

$$\rightarrow_{\mathsf{rule}} (1 \cdot 1) \cdot 2$$

$$0 \leqslant 0$$
 valid

$$\rightarrow_{\rm calc}^+$$
 2

More formally ...

Definition (Logic and Terms)

- \mathcal{F}_T is sorted term signature
- \mathcal{F}_l is sorted logic signature, including boolean operations (true, false, \wedge, \dots) and =
- $Val := \mathcal{F}_l \cap \mathcal{F}_T$ is set of constants called **values**
- **terms** $\mathcal{T}(\mathcal{F}_l, \mathcal{V})$ are called **logical** and associated with fixed interpretation in theory \mathcal{T}

Definition

- **logically constrained rule** is triple $\ell \to r$ $[\varphi]$ where $\text{root}(\ell) \in \mathcal{F}_T \setminus \mathcal{F}_L$ and r has same sort as ℓ , and φ is logical term of sort bool
- LCTRS \mathcal{R} is set of logically constrained rules

Example (LCTRS over theory of integer arithmetic)

- $Val = \{true, false\} \cup \{0, 1, -1, 2, ...\}, \mathcal{F}_L = Val \cup \{+, -, \land, \lor, \cdot\} \text{ and } \mathcal{F}_T = Val \cup \{fact\}\}$
- 0, 1+2, x+(y-1) are logical terms
- Friends Juniba Osk 2024 es AT/SMT Solving and Applications in Rewriting session 4 3. Logically Constrained Term Rewrite Systems
- $fact(x) \rightarrow fact(x-1) \cdot x \quad [x-1 \ge 0]$

Application 1: Program equivalence

Two C programs: are they equivalent?

int sum1(int arr[],int n) { for(int i=0;i<n;i++) ret+=arr[i]; return ret:

int sum2(int *arr, int n) { if (n <= 0) return 0; return arr[n-1] + sum2(arr, n-1);

• programs can be transformed into LCTRS $\mathcal R$ over theory of integer arithmetic and arrays

 $sum1(a, n) \rightarrow u(a, n, 0, 0)$ $u(a, n, r, i) \rightarrow error [i < n \land (i < 0 \lor i > size(a))]$ $u(a, n, r, i) \rightarrow u(a, n, r + select(a, i), i+1) [i < n \land 0 \le i < size({\it s})]$ $u(a, n, r, i) \rightarrow return(a, r) [i > n]$

 $sum2(a, n) \rightarrow return(a, 0) [n < 0]$ $sum2(a, n) \rightarrow error [n-1 \ge size(a)]$

 $s = t [\varphi]$ is inductive theorem if $s\sigma \leftrightarrow_{\mathcal{P}}^* t\sigma$ for all ground constructor substitutions σ that respect φ

- equivalence holds if sum1(a, n) = sum2(a, n) [0 < n < size(a)] is inductive theorem
- inductive theorem proving for LCTRS is implemented in tool Ctrl, based on C. Fuhs, C. Kop, N. Nishida. Verifying Procedural Programs via Constrained Rewriting Induction. ACM Trans. Comput. Log. 18(2), 2017.

Definition (Constrained Rewriting)

- $\mathcal{R}_{calc} = \{ f(x_1, \dots, x_n) \rightarrow y \ [y = f(x_1, \dots, x_n)] \mid f \in \mathcal{F}_L \setminus \mathcal{V}al \}$
- substitution σ respects φ if $\varphi \sigma$ valid and $\sigma(x) \in \mathcal{V}al \ \forall x \in \mathcal{V}ar(\varphi)$
- $C[\ell\sigma] \to_{\mathcal{R}} C[r\sigma]$ for LCTRS \mathcal{R} if $\ell \to r$ $[\varphi] \in \mathcal{R} \cup \mathcal{R}_{calc}$ and σ respects φ

Example

• \mathcal{R}_{calc} contains e.g.

$$x \wedge y \rightarrow z \quad [z = x \wedge y]$$
 $x + y \rightarrow z \quad [z = x + y]$

$$z + y \rightarrow z \quad [z = x + y]$$

for LCTRS R:

(1)
$$f(x) \to a [x \ge 0]$$

(1)
$$f(x) \to a [x \ge 0]$$
 (2) $g(f(x), y) \to g(x, z) [x \ne 0 \land z > x]$

- $f(f(3)) \rightarrow_{\mathcal{R}} f(a)$
- $g(f(1), a) \to_{\mathcal{R}} g(1, 23)$

 $1 \neq 0 \land 23 > 1$ valid

q(f(a), a) is NF

 $x \mapsto a$ not respectful for (2)

• $q(3+2) \to_{\mathcal{R}} q(5)$

5 = 3 + 2 valid

- universität unibz ISR 2024 SAT/SMT Solving and Applications in Rewriting

3. Logically Constrained Term Rewrite Systems

 $3 \geqslant 0$ valid

Application 2: Expression simplification in compilers

The Instcombine pass 0101111101010110110 int x = 4 * (z | 101); 11111111110111010000 1111101110111010010 $\mathsf{mul}(x,C_1) \to \mathsf{shl}(x,C_2) \quad [\log 2\ (C_1) = C_2 \land \mathsf{isPowerOf2}\ (C_1)]$

- LLVM provides widely used compilation toolchain for various programming languages
- Instcombine pass performs >1000 algebraic simplifications of expressions: multiplications to shifts, reordering bitwise operations, ...
- optimization set is community maintained, interference unclear: termination is crucial

Termination analysis via LCTRSs

each simplification can be modeled as LCTRS rewrite rule over bitvector theory

Definition

LCTRS is terminating if $\rightarrow_{\mathsf{rule}} \cup \rightarrow_{\mathsf{calc}}$ is well-founded

(Non-)Termination techniques for LCTRSs in Ctrl

- termination via DP framework and polynomial interpretations
 - C. Kop and N. Nishida. Constrained Term Rewriting tool. Proc. 20th LPAR, pp. 549-557, 2015.
- non-termination via loops
 - N. Nishida and S. Winkler. Loop Detection by Logically Constrained Term Rewriting. Proc. 10th VSTTE, pp. 309-321, 2018.

Example (Loop in Instcombine simplification set)

rewrite rule

$$\mathsf{mul}(\mathsf{sub}(y,x),z) \to \mathsf{mul}(\mathsf{sub}(x,y),\mathsf{abs}(z)) \ [z < \mathbf{0}_8 \land \mathsf{isPowerOf2}(\mathsf{abs}(z))]$$

admits loop

$$\begin{aligned} \text{mul}(\text{sub}(\textbf{1}_8,\textbf{1}_8),(\textbf{-128})_8) &\rightarrow \text{mul}(\text{sub}(\textbf{1}_8,\textbf{1}_8),\text{abs}((\textbf{-128})_8)) \\ &\rightarrow_{\text{calc}} \text{mul}(\text{sub}(\textbf{1}_8,\textbf{1}_8),(\textbf{-128})_8) \end{aligned}$$



ISR 2024 SAT/SMT Solving and Applications in Rewriting session 4

3. Logically Constrained Term Rewrite Systems Applications

Further Reading

- Akihisa Yamada
- Term Orderings for Non-reachability of (Conditional) Rewriting Proceedings IJCAR 2022, LNAI 13385, pp. 248-267, 2022
- Takahito Aoto
 - Disproving Confluence of Term Rewriting Systems by Interpretation and Ordering Proceedings FroCoS 2013, LNAI 8152,pp. 311-326, 2013





19/19

Outline

- 1. Solution of Exercise of Session 3
- 2. Beyond Reduction Orders and Termination
- 3. Logically Constrained Term Rewrite Systems
- 4. Further Reading



