



Specialisation Seminar

Abstraction Interpretations

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A Textbook Example

Example

```
let rec fold_left f acc = function
  [] -> acc
  | x::xs -> fold_left f (f acc x) xs
;;
let rev ls = fold_left (fun xs x -> x::xs) [] ls
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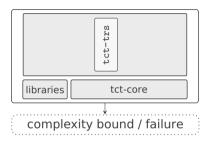
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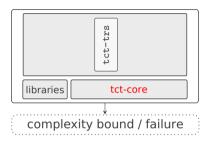
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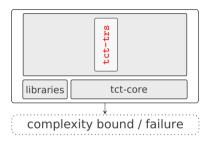
T_CT says



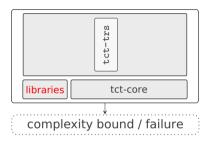
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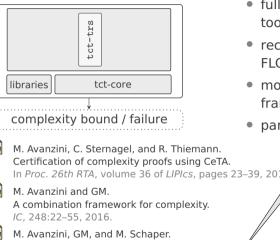
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M. Avanzini, GM, and M. Schaper. Tct: Tyrolean complexity tool. In *Proc. 22nd TACAS*, volume 9636 of *LNCS*, pages 407–423, 2016.

Does Testing Suffice?

Problems

- only samples the set of possible behaviors
- unlike physical systems, software systems are discontinuous
- there is no sound basis for extrapolating from tested to untested cases
- need to consider all (= infinitely many cases) ... is this possible?
- it's possible with symbolic techniques
- for example Abstract Interpretations
- more on that in the seminar

Importance of Static Analysis

In the Beginning

- static analysis initially used for optimising compilers
- program transformations preserve the behavior
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Today

- bug finding
- program understanding
- verification
- resource analysis

The Phases of GCC

- parsing
- tree optimisation
- RTL generation
- sibling call optimisation
- jump optimisation
- register scan
- jump threading
- common subexpression elimination
- loop optimisations
- jump bypassing
- data flow analysis

- instruction combination
- if-conversion
- register movement
- instruction scheduling
- register allocation
- basic block reordering
- delayed branch scheduling
- branch shortening
- assembly output
- debugging output

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60% of the compiliation time spent in static analysis

Availabe Expression Analysis

```
i := 0;
while (i <= n) {
    j := 0;
    while (j <= m) {
        A[i*(m+1)+j] := B[i*(m+1)+j] + C[i*(m+1)+j] ;
        j := j+1; }
    i := i+1; }
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Common Subexpression Elimination: Introduction of Temp Vars

```
i := 0;
while (i <= n) {
    j := 0;
    while (j <= m) {
        temp := i*(m+1)+j;
        A[temp] := B[temp] + C[temp] ;
        j := j+1; }
    i := i+1; }
```

Theoretical Results

Theorem (Rice's Theorem)

Interesting Program Properties are undecidable.

Proof.

See FLAT

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Undecidabilty

- static analysis is (very) undecidable, that is necessarily unsound or incomplete or partial
- engineering challenge is to give practical useful answers anyway

Labelled Programs

Definitions	(syntactic categories)
-------------	------------------------

$a\in \mathbf{AExp}$	arithmetic expressions	$x,y\in Var$	variables
$b\in {f BExp}$	Boolean expressions	$n \in \mathbf{Num}$	numerals
$S \in \mathbf{STmt}$	statments	$\ell\inLab$	labels

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Definitions (operators)

- $op_a \in \mathbf{Op}_a$ arithmetic operators
- $op_b \in \mathbf{Op}_b$ Boolean operators
- $op_r \in \mathbf{Op}_r$ relational operators

Abstract Syntax

Definition (syntax of WHILE)

 $\begin{aligned} a ::= x \mid n \mid a_1 \ op_a \ b_2 \\ b ::= \mathbf{true} \mid \mathbf{false} \mid \mathbf{not} \ b \mid b_1 \ op_b \ b_2 \mid a_1 \ op_r \ a_2 \\ S ::= [x := a]^{\ell} \mid [\mathbf{skip}]^{\ell} \mid S_1; S_2 \mid \mathbf{if} \ [b]^{\ell} \ \mathbf{then} \ S_1 \mathbf{else} \ S_2 \mid \mathbf{while} \ [b]^{\ell} \ \mathbf{do} \ S \end{aligned}$

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Konvention

- labelled items are referred to as elementary blocks
- we shall use paranteses to disambiguate the syntax
- for statements we write {, } or begin, end
- for other categories we use round brackets

Reaching Definition Analysis

Definition

An assignment (aka definition) of the from $[x := a]^{\ell}$ may reach a certain program point if there is an execution of the program where x was last assigned a value at ℓ when the program point is reached

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Definitions

- (y, 1) reaches the entry to 2, if the assignment [y := x]¹ reaches the entry of the elementary block [z := 1]²
- we also say that (x, ?) reaches 2; here ? stands for an uninitialised value

full information about reaching definitions is given by the pair $RD = (RD_{entry}, RD_{exit})$ for each program location

Data Flow Analysis The Equational Approach

an analysis (like reaching definitions) can be specified by extracting a number of equations from the program

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Equation System

there are two classes of equations:

- relate exit information of a block to entry information of the same block
- relate entry information of a block to the exit information of the preceeding block (wrt the CFG)
- the CFG may be immediately obvious (as in the WHILE language) or may be obtained by a control flow analysis