

SAT and SMT Solving

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lecture 4
SS 2019

Outline

- Summary of Last Week
- Unsatisfiable Cores
- Algorithm by Fu and Malik
- Unsatisfiable Cores in Practice

Maximum Satisfiability

Consider CNF formulas χ and φ as sets of clauses.

Definitions

- ▶ $\text{maxSAT}(\varphi)$ is maximal $\sum_{C \in \varphi} \bar{v}(C)$ for valuation v
- ▶ $\text{pmaxSAT}(\varphi, \chi)$ is maximal $\sum_{C \in \varphi} \bar{v}(C)$ for valuation v with $v(\chi) = \text{T}$

Definitions

given weights $w_C \in \mathbb{Z}$ for all $C \in \varphi$,

- ▶ $\text{maxSAT}_w(\varphi)$ is maximal $\sum_{C \in \varphi} w_C \cdot \bar{v}(C)$ for valuation v ?
- ▶ $\text{pmaxSAT}_w(\varphi, \chi)$ is maximal $\sum_{C \in \varphi} w_C \cdot \bar{v}(C)$ for valuation v with $v(\chi) = \text{T}$

Definition

$\text{minUNSAT}(\varphi)$ is minimal $\sum_{C \in \varphi} \bar{v}(\neg C)$ for valuation v

Lemma

$$|\varphi| = |\text{minUNSAT}(\varphi)| + |\text{maxSAT}(\varphi)|$$

Branch & Bound

Idea

- ▶ gets list of clauses φ as input return $\text{minUNSAT}(\varphi)$
- ▶ explores assignments in depth-first search

```
function BnB( $\varphi$ , UB)
   $\varphi$  = simp( $\varphi$ )
  if  $\varphi$  contains only empty clauses then
    return #empty( $\varphi$ )
  if #empty( $\varphi$ )  $\geq$  UB then
    return UB
  x = selectVariable( $\varphi$ )
  UB' = min(UB, BnB( $\varphi_x$ , UB))
  return min(UB', BnB( $\varphi_{\bar{x}}$ , UB'))
```

Theorem

$$\text{BnB}(\varphi, |\varphi|) = \text{minUNSAT}(\varphi)$$

Binary Search

Idea

- ▶ gets list of clauses φ as input and returns $\text{minUNSAT}(\varphi)$
- ▶ repeatedly call SAT solver in binary search fashion

Definitions

- ▶ **cardinality constraint** is

$$\sum_{x \in X} x \bowtie N$$

where \bowtie is $=$, $<$, $>$, \leq , or \geq , X is set of propositional variables, and $N \in \mathbb{N}$

- ▶ valuation v satisfies $\sum_{x \in X} x \bowtie N$ iff $k \bowtie N$
where k is number of variables $x \in X$ such that $v(x) = \text{T}$

Remark

cardinality constraints are expressible in CNF

Algorithm (Binary Search)

```
function BinarySearch( $\{C_1, \dots, C_m\}$ )  
   $\varphi := \{C_1 \vee b_1, \dots, C_m \vee b_m\}$   
  return search( $\varphi, 0, m$ )
```

b_1, \dots, b_m are fresh variables

```
function search( $\varphi, L, U$ )  
  if  $L \geq U$  then  
    return U  
   $\text{mid} := \lfloor \frac{U+L}{2} \rfloor$   
  if SAT( $\varphi \wedge \text{CNF}(\sum_{i=1}^m b_i \leq \text{mid})$ ) then  
    return search( $\varphi, L, \text{mid}$ )  
  else  
    return search( $\varphi, \text{mid} + 1, U$ )
```

Theorem

$\text{BinarySearch}(\varphi) = \text{minUNSAT}(\varphi)$

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for **unsatisfiable CNF formula** φ given as set of clauses

- ▶ $\psi \subseteq \varphi$ such that $\bigwedge_{C \in \psi} C$ is unsatisfiable is **unsatisfiable core (UC)** of φ

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Example

$$\varphi = \{\neg x, \quad x \vee z, \quad \neg y \vee \neg z, \quad x, \quad y \vee \neg z\}$$

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unsatisfiable cores are

- ▶ φ
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- ▶ $\{ \neg x, x \}$ minimal and SUC

Remark

SUC is always minimal unsatisfiable core

Example

$$\varphi = \{C_1, \dots, C_6\}$$

$$C_1: x_1 \vee \neg x_3$$

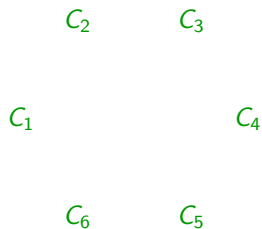
$$C_2: x_2$$

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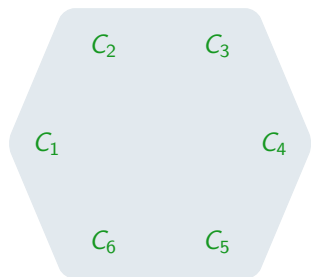
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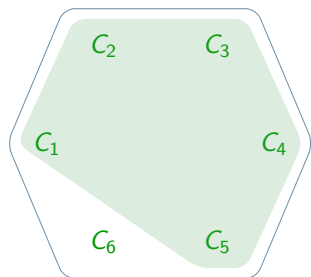
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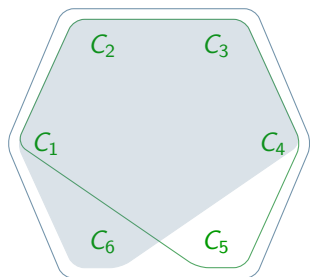
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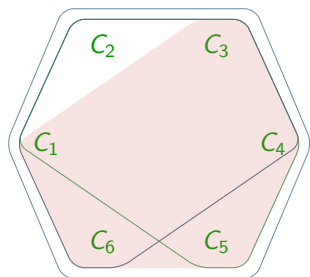
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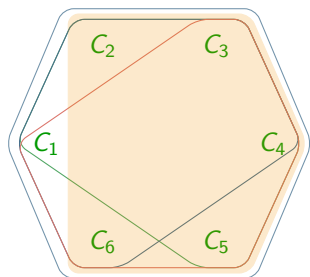
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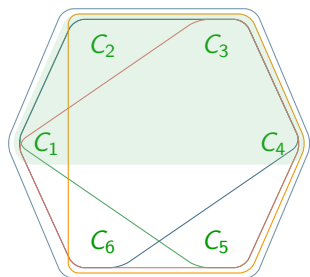
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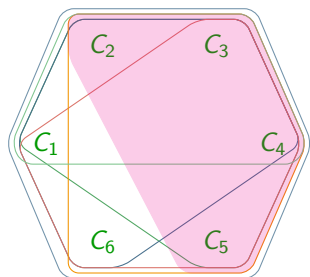
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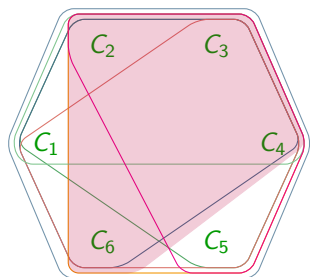
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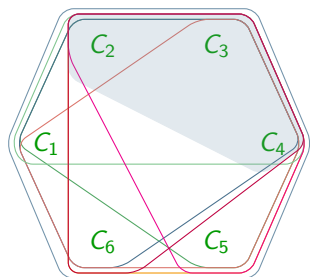
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minimal and SUC

Application: FPGA Routing

Field Programmable Gate Arrays (FPGAs)

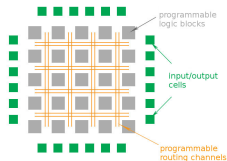
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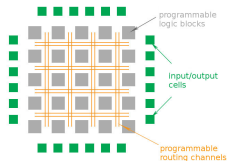
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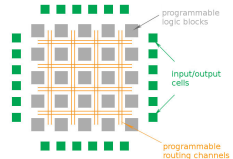
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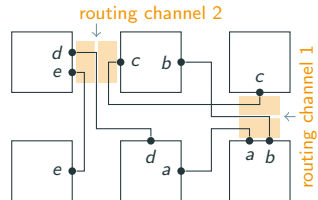
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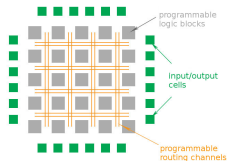
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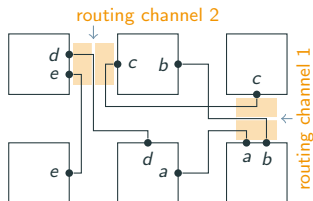
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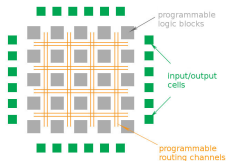
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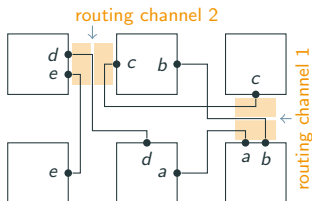
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Example (Encoding Routing Requirements)

- ▶ consider connections a, b, c, d, e of 2 bits each
- ▶ liveness: want to route ≥ 1 bit of a, b, c, d, e
- ▶ 2 routing channels of 2 tracks each
- ▶ **exclusivity**: each channel has only 2 tracks

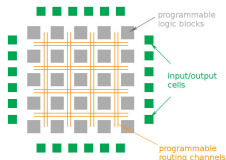
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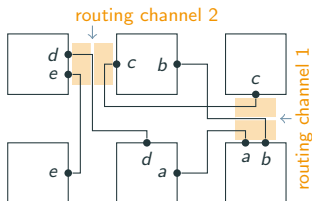
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- ▶ **unsatisfiable**: UCs indicate problems

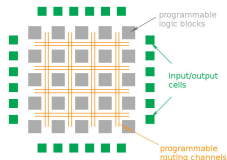
$$\begin{array}{lll} a_0 \vee a_1 & \neg a_0 \vee \neg b_0 & \neg c_0 \vee \neg d_0 \\ b_0 \vee b_1 & \neg a_0 \vee \neg c_0 & \neg c_0 \vee \neg e_0 \\ c_0 \vee c_1 & \neg b_0 \vee \neg c_0 & \neg d_0 \vee \neg e_0 \\ d_0 \vee d_1 & \neg a_1 \vee \neg b_1 & \neg c_1 \vee \neg d_1 \\ e_0 \vee e_1 & \neg a_1 \vee \neg c_1 & \neg c_1 \vee \neg e_1 \\ & \neg b_1 \vee \neg c_1 & \neg d_1 \vee \neg e_1 \end{array}$$



Application: FPGA Routing

Field Programmable Gate Arrays (FPGAs)

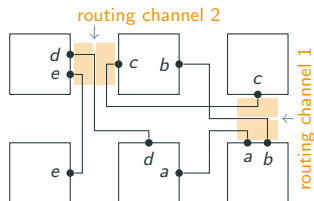
- ▶ can simulate microprocessors but faster for special tasks (from complex combinatorics to mere logic)
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Example (Encoding Routing Requirements)

- ▶ consider connections a, b, c, d, e of 2 bits each
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$e_0 \vee e_1$		$\neg a_1 \vee \neg c_1$	●	$\neg c_1 \vee \neg e_1$
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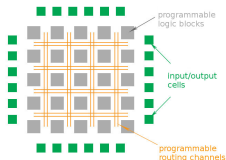


UC1: channel 1 capacity exceeded

Application: FPGA Routing

Field Programmable Gate Arrays (FPGAs)

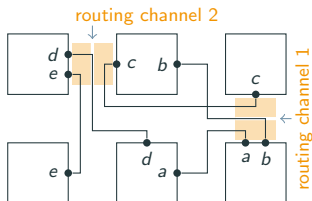
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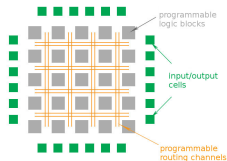
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Application: FPGA Routing

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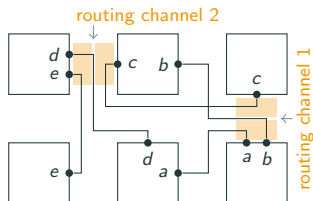
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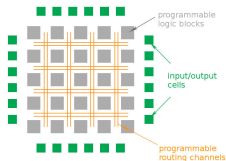


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Application: FPGA Routing

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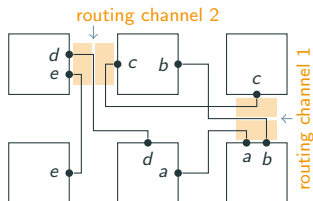
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Finding Minimal Unsatisfiable Cores by Resolution

Idea

- ▶ repeatedly pick clause C from φ and check satisfiability:
if $\varphi \setminus \{C\}$ is satisfiable, keep C , otherwise drop C

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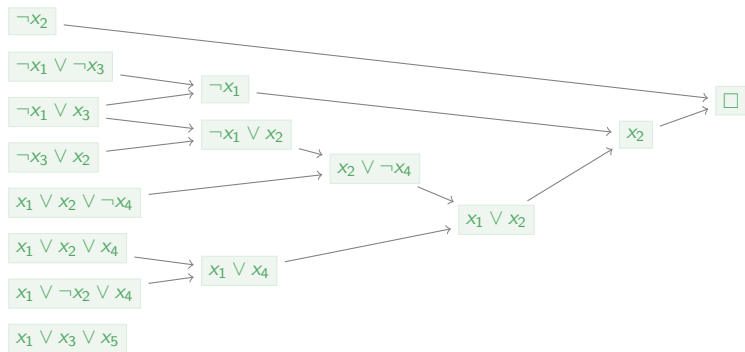
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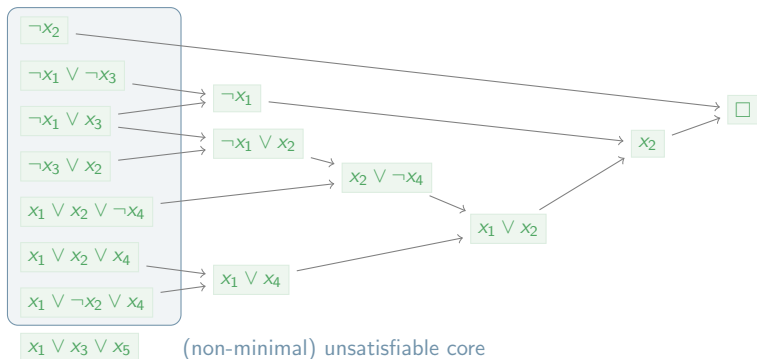


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- ▶ \overline{N} is $V \setminus N$ for any set of nodes N

Algorithm minUnsatCore(φ)

Input: unsatisfiable formula φ **Output:** minimal unsatisfiable core of φ build resolution graph $G = (V_i \uplus V_c, E)$ for φ **while** \exists unmarked clause in V_i **do** $C \leftarrow$ unmarked clause in V_i **if** SAT($\overline{Reach_G(C)}$) **then**mark C \triangleright subgraph without C satisfiable? $\triangleright C$ is UC member**else**build resolution graph $G' = (V'_i \uplus V'_c, E')$ for $\overline{Reach_G(C)}$ $V_i \leftarrow V_i \setminus \{C\}$ and $V_c \leftarrow V'_c \cup (V_c \setminus Reach_G(C))$ $E \leftarrow E' \cup (E \setminus Reach_G^E(C))$ $G \leftarrow (V_i \cup V_c, E)$ $G \leftarrow G|_{\square}$ \triangleright restrict to nodes with path to \square return V_i

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Algorithm minUnsatCore(φ)

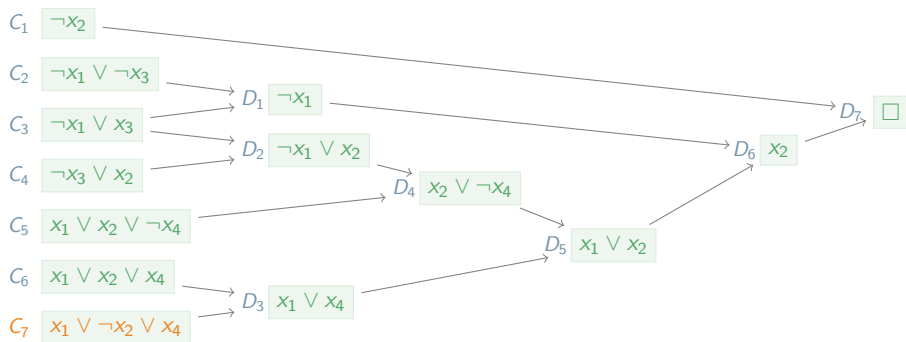
Input: unsatisfiable formula φ **Output:** minimal unsatisfiable core of φ build resolution graph $G = (V_i \uplus V_c, E)$ for φ **while** \exists unmarked clause in V_i **do** $C \leftarrow$ unmarked clause in V_i **if** SAT($\overline{Reach_G(C)}$) **then**mark C \triangleright subgraph without C satisfiable? $\triangleright C$ is UC member**else**build resolution graph $G' = (V'_i \uplus V'_c, E')$ for $\overline{Reach_G(C)}$ $V_i \leftarrow V_i \setminus \{C\}$ and $V_c \leftarrow V'_c \cup (V_c \setminus Reach_G(C))$ $E \leftarrow E' \cup (E \setminus Reach_G^E(C))$ $G \leftarrow (V_i \cup V_c, E)$ $G \leftarrow G|_{\square}$ \triangleright restrict to nodes with path to \square return V_i

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Theorem*if φ unsatisfiable then $\text{minUnsatCore}(\varphi)$ is minimal unsatisfiable core of φ*

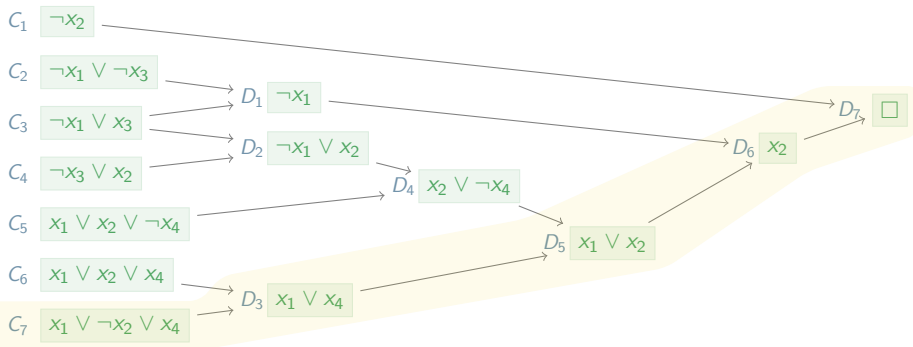
Example



minUnsatCore(φ)

- pick C_7

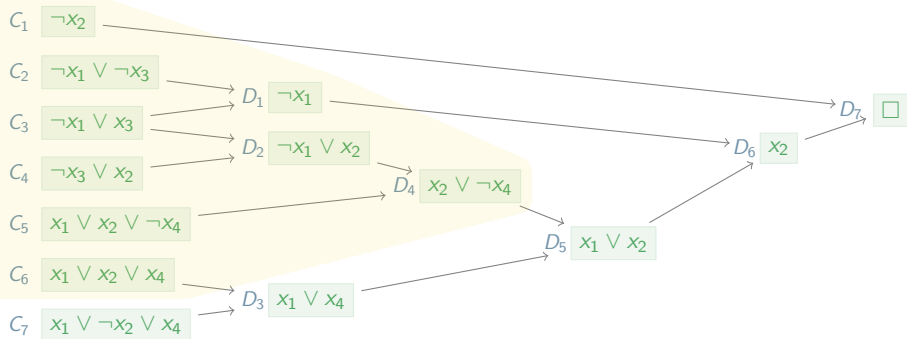
Example



$\text{minUnsatCore}(\varphi)$

- ▶ pick C_7
- ▶ $\text{Reach}_G(C_7) = \{C_7, D_3, D_5, D_6, D_7\}$

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Example

$$C_1 \quad \neg x_2$$

$$C_2 \quad \neg x_1 \vee \neg x_3$$

$$C_3 \quad \neg x_1 \vee x_3$$

$$C_4 \quad \neg x_3 \vee x_2$$

$$C_5 \quad x_1 \vee x_2 \vee \neg x_4$$

$$C_6 \quad x_1 \vee x_2 \vee x_4$$

$$D_1 \quad \neg x_1$$

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$$D_4 \quad x_2 \vee \neg x_4$$

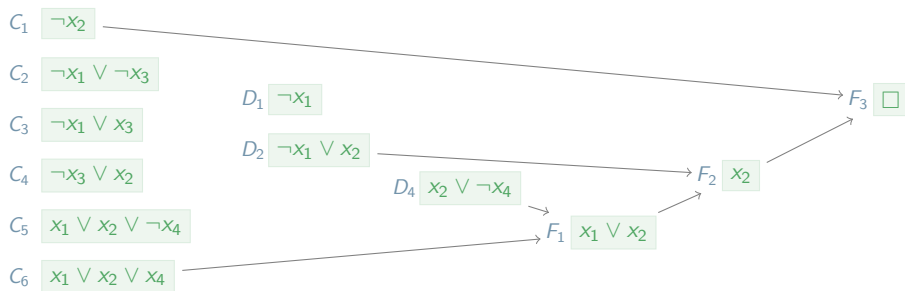
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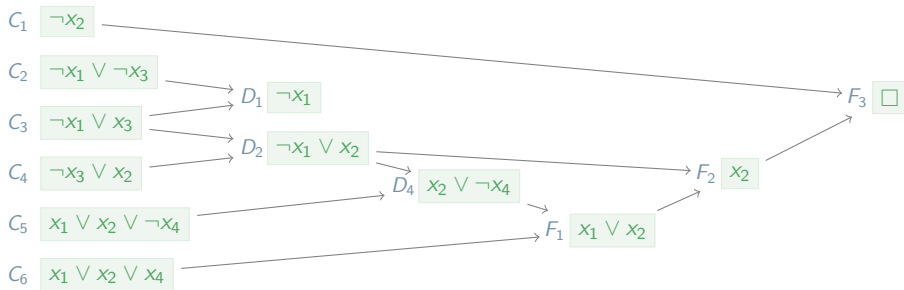
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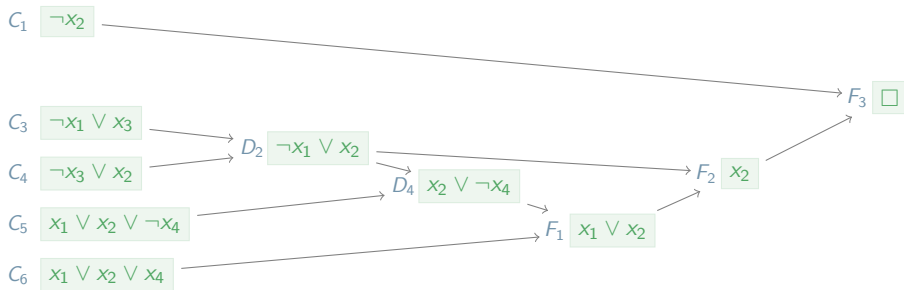
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- ▶ construct resolution graph G' for φ by adding edges from G to G_7

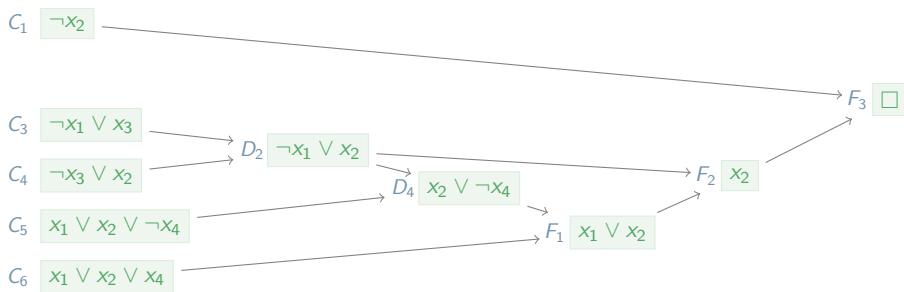
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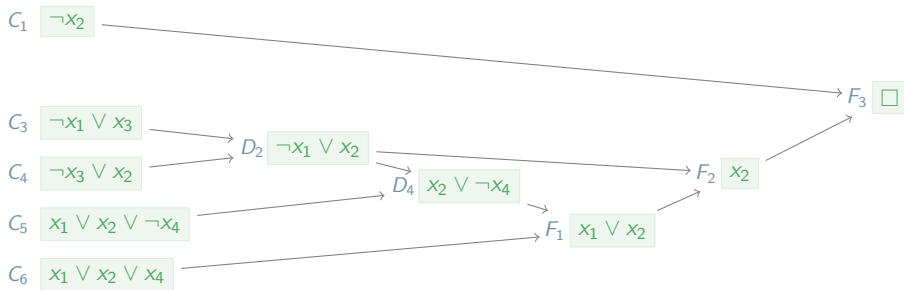
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re-use relevant resolvents:
fewer steps to \square

Outline

- Summary of Last Week
- Unsatisfiable Cores
- Algorithm by Fu and Malik
- Unsatisfiable Cores in Practice

Bounds for Maximum Satisfiability

consider CNF formula $\varphi = C_1 \wedge \cdots \wedge C_m$

Definition

blocked formula is $\varphi_B = (C_1 \vee b_1) \wedge \cdots \wedge (C_m \vee b_m)$ for fresh variables b_1, \dots, b_m

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Example (Upper Bound)

$$\begin{array}{ccccccc} \neg x_3 \vee \neg x_4 & \neg x_3 \vee x_4 & & x_7 & & & \\ & x_3 & & \neg x_7 \vee x_8 & \neg x_1 \vee x_8 & & x_4 \vee x_5 \quad x_1 \vee \neg x_5 \vee x_6 \\ & x_1 & \neg x_1 \vee \neg x_3 & \neg x_7 \vee \neg x_8 \vee x_6 & \neg x_9 \vee x_2 & & x_5 \vee \neg x_6 \\ \neg x_1 \vee \neg x_2 & \neg x_1 \vee x_2 & & \neg x_7 \vee \neg x_8 \vee \neg x_6 & & & \neg x_4 \vee x_5 \quad \neg x_1 \vee \neg x_5 \end{array}$$

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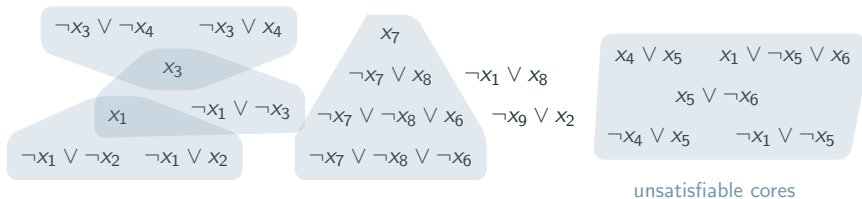
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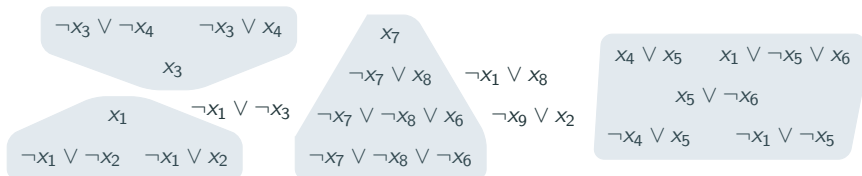
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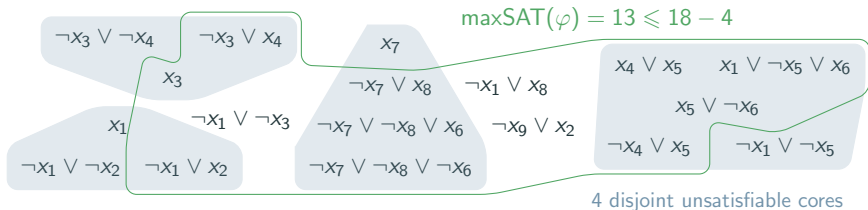
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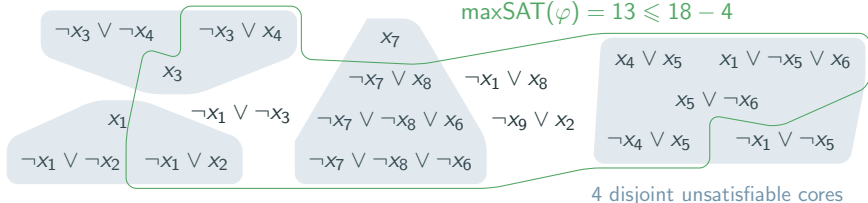
Lemma (Upper Bound)

if φ contains k disjoint unsatisfiable cores then $\max\text{SAT}(\varphi) \leq m - k$

Example (Upper Bound)

must miss at least one clause from every core!

$$\max\text{SAT}(\varphi) = 13 \leq 18 - 4$$



Idea

- ▶ **maxsat** valuation must make at least **one clause** in unsatisfiable core **false**

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Algorithm by Fu and Malik

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Lemma

$$|\varphi| = |\text{pminUNSAT}(\chi, \varphi)| + |\text{pmaxSAT}(\chi, \varphi)|$$

Algorithm FuMalik(χ, φ)

Input: soft clauses φ and satisfiable hard clauses χ

Output: pminUNSAT(χ, φ)

$cost \leftarrow 0$

while \neg SAT($\chi \cup \varphi$) **do**

$UC \leftarrow$ unsatCore($\chi \cup \varphi$)

$B \leftarrow \emptyset$

for $C \in UC \cap \varphi$ **do**

▷ loop over soft clauses in core

$b \leftarrow$ fresh “blocking” variable

$\varphi \leftarrow \varphi \setminus \{C\} \cup \{C \vee b\}$

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▷ cardinality constraint is hard

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Theorem

$\text{FuMalik}(\chi, \varphi) = \text{pminUNSAT}(\chi, \varphi)$

Example

$$\chi: \neg x_1 \vee x_3$$

$$\varphi: \neg x_1 \vee \neg x_2$$

$$\neg x_3 \vee x_4$$

$$\neg x_4 \vee x_5$$

$$\neg x_7 \vee x_8$$

$$\neg x_7 \vee x_2$$

$$\neg x_1 \vee x_2$$

$$x_3$$

$$x_1 \vee \neg x_5 \vee x_6$$

$$\neg x_7 \vee \neg x_8 \vee x_6$$

$$x_7 \vee x_2$$

$$\neg x_1 \vee x_7$$

$$\neg x_3 \vee \neg x_4$$

$$x_5 \vee \neg x_6$$

$$\neg x_7 \vee \neg x_8 \vee \neg x_6$$

$$x_1 \vee \neg x_2$$

$$x_1$$

$$x_4 \vee x_5$$

$$x_7$$

$$\neg x_1 \vee \neg x_3$$

Example

χ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
φ :	$\neg x_1 \vee \neg x_2$	$\neg x_1 \vee x_2$	$\neg x_1 \vee x_7$	x_1
	$\neg x_3 \vee x_4$	x_3	$\neg x_3 \vee \neg x_4$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	x_7
	$\neg x_7 \vee x_8$	$\neg x_7 \vee \neg x_8 \vee x_6$	$\neg x_7 \vee \neg x_8 \vee \neg x_6$	$\neg x_1 \vee \neg x_3$

- unsatisfiable core: $\neg x_1 \vee \neg x_2$, $\neg x_1 \vee x_2$, x_1

Example

χ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
φ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4$	x_3	$\neg x_3 \vee \neg x_4$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	x_7
	$\neg x_7 \vee x_8$	$\neg x_7 \vee \neg x_8 \vee x_6$	$\neg x_7 \vee \neg x_8 \vee \neg x_6$	$\neg x_1 \vee \neg x_3$

- unsatisfiable core: $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$
cost = 1

Example

χ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
φ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4$	x_3	$\neg x_3 \vee \neg x_4$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	x_7
	$\neg x_7 \vee x_8$	$\neg x_7 \vee \neg x_8 \vee x_6$	$\neg x_7 \vee \neg x_8 \vee \neg x_6$	$\neg x_1 \vee \neg x_3$

- ▶ unsatisfiable core: $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$
 $\text{cost} = 1$
- ▶ unsatisfiable core: $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$

Example

χ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
φ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	x_7
	$\neg x_7 \vee x_8$	$\neg x_7 \vee \neg x_8 \vee x_6$	$\neg x_7 \vee \neg x_8 \vee \neg x_6$	$\neg x_1 \vee \neg x_3$

- ▶ unsatisfiable core: $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$
 $\text{cost} = 1$
- ▶ unsatisfiable core: $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$
 $\text{cost} = 2$

Example

χ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
φ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	x_7
	$\neg x_7 \vee x_8$	$\neg x_7 \vee \neg x_8 \vee x_6$	$\neg x_7 \vee \neg x_8 \vee \neg x_6$	$\neg x_1 \vee \neg x_3$

- ▶ unsatisfiable core: $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$
 $\text{cost} = 1$
- ▶ unsatisfiable core: $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$
 $\text{cost} = 2$
- ▶ unsatisfiable core: $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$

Example

$$\begin{array}{llll} \chi: & \neg x_1 \vee x_3 & \neg x_7 \vee x_2 & x_7 \vee x_2 & x_1 \vee \neg x_2 \\ \varphi: & \neg x_1 \vee \neg x_2 \vee b_1 & \neg x_1 \vee x_2 \vee b_2 & \neg x_1 \vee x_7 & x_1 \vee b_3 \\ & \neg x_3 \vee x_4 \vee c_1 & x_3 \vee c_2 & \neg x_3 \vee \neg x_4 \vee c_3 & x_4 \vee x_5 \\ & \neg x_4 \vee x_5 & x_1 \vee \neg x_5 \vee x_6 & x_5 \vee \neg x_6 & x_7 \vee d_1 \\ & \neg x_7 \vee x_8 \vee d_2 & \neg x_7 \vee \neg x_8 \vee x_6 \vee d_3 & \neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4 & \neg x_1 \vee \neg x_3 \end{array}$$

- ▶ unsatisfiable core: $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$
 $\text{cost} = 1$
- ▶ unsatisfiable core: $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$
 $\text{cost} = 2$
- ▶ unsatisfiable core: $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$
 $\text{cost} = 3$

Example

χ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
φ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7 \vee d_1$
	$\neg x_7 \vee x_8 \vee d_2$	$\neg x_7 \vee \neg x_8 \vee x_6 \vee d_3$	$\neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4$	$\neg x_1 \vee \neg x_3$

- ▶ unsatisfiable core: $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$
 $\text{cost} = 1$
- ▶ unsatisfiable core: $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$
 $\text{cost} = 2$
- ▶ unsatisfiable core: $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$
 $\text{cost} = 3$
- ▶ unsatisfiable core: $\neg x_1 \vee x_3, \neg x_7 \vee x_2, x_7 \vee x_2, x_1 \vee \neg x_2, \neg x_1 \vee \neg x_3$

Example

$$\begin{array}{llll} \chi: & \neg x_1 \vee x_3 & \neg x_7 \vee x_2 & x_7 \vee x_2 & x_1 \vee \neg x_2 \\ \varphi: & \neg x_1 \vee \neg x_2 \vee b_1 & \neg x_1 \vee x_2 \vee b_2 & \neg x_1 \vee x_7 & x_1 \vee b_3 \\ & \neg x_3 \vee x_4 \vee c_1 & x_3 \vee c_2 & \neg x_3 \vee \neg x_4 \vee c_3 & x_4 \vee x_5 \\ & \neg x_4 \vee x_5 & x_1 \vee \neg x_5 \vee x_6 & x_5 \vee \neg x_6 & x_7 \vee d_1 \\ & \neg x_7 \vee x_8 \vee d_2 & \neg x_7 \vee \neg x_8 \vee x_6 \vee d_3 & \neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4 & \neg x_1 \vee \neg x_3 \vee e_1 \end{array}$$

- ▶ unsatisfiable core: $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$
 $\text{cost} = 1$
- ▶ unsatisfiable core: $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$
 $\text{cost} = 2$
- ▶ unsatisfiable core: $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$
 $\text{cost} = 3$
- ▶ unsatisfiable core: $\neg x_1 \vee x_3, \neg x_7 \vee x_2, x_7 \vee x_2, x_1 \vee \neg x_2, \neg x_1 \vee \neg x_3$
 $\chi = \chi \cup \text{CNF}(e_1 = 1)$
 $\text{cost} = 4$

Example

$$\begin{array}{llll} \chi: & \neg x_1 \vee x_3 & \neg x_7 \vee x_2 & x_7 \vee x_2 & x_1 \vee \neg x_2 \\ \varphi: & \neg x_1 \vee \neg x_2 \vee b_1 & \neg x_1 \vee x_2 \vee b_2 & \neg x_1 \vee x_7 & x_1 \vee b_3 \\ & \neg x_3 \vee x_4 \vee c_1 & x_3 \vee c_2 & \neg x_3 \vee \neg x_4 \vee c_3 & x_4 \vee x_5 \\ & \neg x_4 \vee x_5 & x_1 \vee \neg x_5 \vee x_6 & x_5 \vee \neg x_6 & x_7 \vee d_1 \\ & \neg x_7 \vee x_8 \vee d_2 & \neg x_7 \vee \neg x_8 \vee x_6 \vee d_3 & \neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4 & \neg x_1 \vee \neg x_3 \vee e_1 \end{array}$$

- ▶ unsatisfiable core: $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$
 $\text{cost} = 1$
- ▶ unsatisfiable core: $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$
 $\text{cost} = 2$
- ▶ unsatisfiable core: $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$
 $\text{cost} = 3$
- ▶ unsatisfiable core: $\neg x_1 \vee x_3, \neg x_7 \vee x_2, x_7 \vee x_2, x_1 \vee \neg x_2, \neg x_1 \vee \neg x_3$
 $\chi = \chi \cup \text{CNF}(e_1 = 1)$
 $\text{cost} = 4$
- ▶ satisfiable

Example

$$\begin{array}{llll} \chi: & \neg x_1 \vee x_3 & \neg x_7 \vee x_2 & x_7 \vee x_2 & x_1 \vee \neg x_2 \\ \varphi: & \neg x_1 \vee \neg x_2 \vee b_1 & \neg x_1 \vee x_2 \vee b_2 & \neg x_1 \vee x_7 & x_1 \vee b_3 \\ & \neg x_3 \vee x_4 \vee c_1 & x_3 \vee c_2 & \neg x_3 \vee \neg x_4 \vee c_3 & x_4 \vee x_5 \\ & \neg x_4 \vee x_5 & x_1 \vee \neg x_5 \vee x_6 & x_5 \vee \neg x_6 & x_7 \vee d_1 \\ & \neg x_7 \vee x_8 \vee d_2 & \neg x_7 \vee \neg x_8 \vee x_6 \vee d_3 & \neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4 & \neg x_1 \vee \neg x_3 \vee e_1 \end{array}$$

- ▶ unsatisfiable core: $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$
 $\text{cost} = 1$
- ▶ unsatisfiable core: $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$
 $\text{cost} = 2$
- ▶ unsatisfiable core: $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$
 $\text{cost} = 3$
- ▶ unsatisfiable core: $\neg x_1 \vee x_3, \neg x_7 \vee x_2, x_7 \vee x_2, x_1 \vee \neg x_2, \neg x_1 \vee \neg x_3$
 $\chi = \chi \cup \text{CNF}(e_1 = 1)$
 $\text{cost} = 4$
- ▶ satisfiable
- ▶ $\text{pminUNSAT}(\chi, \varphi) = 4$

Example

χ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
φ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7 \vee d_1$
	$\neg x_7 \vee x_8 \vee d_2$	$\neg x_7 \vee \neg x_8 \vee x_6 \vee d_3$	$\neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4$	$\neg x_1 \vee \neg x_3 \vee e_1$

- ▶ unsatisfiable core: $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$
 $\text{cost} = 1$
- ▶ unsatisfiable core: $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$
 $\text{cost} = 2$
- ▶ unsatisfiable core: $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$
 $\text{cost} = 3$
- ▶ unsatisfiable core: $\neg x_1 \vee x_3, \neg x_7 \vee x_2, x_7 \vee x_2, x_1 \vee \neg x_2, \neg x_1 \vee \neg x_3$
 $\chi = \chi \cup \text{CNF}(e_1 = 1)$
 $\text{cost} = 4$
- ▶ satisfiable: $v(x_1) = v(x_2) = v(x_3) = v(x_5) = v(x_7) = \text{T}$ and $v(x_i) = \text{F}$ otherwise
- ▶ $\text{pminUNSAT}(\chi, \varphi) = 4$ and $\text{pmaxSAT}(\chi, \varphi) = 12$

Unsatisfiable Cores in z3

```
from z3 import *

x1,x2,x3 = Bool("x1"), Bool("x2"), Bool("x3")
phi = [ Or(Not(x1), Not(x2)), Or(Not(x1), x2),\
        Or(Not(x1), x3), x1, Or(Not(x3), x2)]

solver = Solver()
solver.set(unsat_core=True)

# assert clauses in phi with names phi0 ... phi4
for i,c in enumerate(phi):
    solver.assert_and_track(c, "phi" + str(i))

if solver.check() == z3.unsat:
    uc = solver.unsat_core()
    print(uc) # [phi0, phi1, phi3]
```



Nachum Dershowitz, Ziyad Hanna, and Alexander Nadel.

A Scalable Algorithm for Minimal Unsatisfiable Core Extraction.

Proc. Theory and Applications of Satisfiability Testing, pp. 36–41, 2006.



Yoonna Oh, Maher Mneimneh, Zaher Andraus, Karem Sakallah, and Igor Markov

AMUSE: A Minimally-Unsatisfiable Subformula Extractor.

Proc. 41st Design Automation Conference, pp. 518–523, 2004.



Zhaohui Fu and Sharad Malik.

On solving the partial MAX-SAT problem.

In Proc. Theory and Applications of Satisfiability Testing, pp. 252–265, 2006