



Program Verification

Part 7 – Summary and Outlook

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Summary of Course

Summary by Parts

- part 2: extend **first-order logic** (of Logic course) by **types**
- part 3: define **standard model** for **functional programs**;
derive **axioms** for induction, equality of constructors, etc.
- part 4: methods to ensure **well-definedness of functional programs**, including automated **termination analysis**
- part 5: framework for **induction proofs and equational reasoning**; **specifications can be given via functional programs**
- part 6: verification of **imperative programs** via **Hoare-calculus**;
includes formal **semantics** and proof of **soundness of calculus**

Summary of Course

Summary by Methodology

- **inductively defined sets**
- proofs by **induction** in various settings
(by algorithm, by data-structure, by inductively defined set, ...)
- proofs by **invariants**
- verification by **refinement**
 - prove soundness of (abstract) pseudo-code against specification
 - prove that concrete code is valid implementation pseudo-code
- integrating **external tools** and **certification**
termination proofs via SMT-solver, logic-solver for Hoare-calculus
- development of **paper-verified interpreter for functional programs**
written in Haskell
 - checks well-definedness of input (missing: termination analyser)
 - algorithms for these checks have been verified
 - verified implementation of one-step evaluation \leftrightarrow

Summary of Course

Feedback

- feedback is highly welcome (via mail, anonymous via PV-website, via evaluation, etc.)
 - content + structure
 - feasibility
 - typos
 - ...

Outlook

Related Courses

- backend-solvers: constraint solving, automated theorem proving
- core evaluation mechanism: (selected topics in) term rewriting
- program verification with tool support: interactive theorem proving
- more automation: program analysis

Related Bachelor Thesis Topics

- recently finished
 - decision procedure for termination of right-ground term rewrite systems (verified)
 - implementing multiset-comparisons (verified SAT-encoding)
 - translation of multitape Turing machines into singletape TMs (partly verified)
- ongoing
 - efficient implementation of weighted path order (verified)
- available
 - automation of rewriting induction with machine checkable proof generation (for certification)
 - not yet announced: preprocessing of linear integer constraints (partly verified)
 - always: contact me with your own ideas on program verification related topics

Thank you for your interest!