

Towards a Hardware-Parallel Implementation of Interaction Nets

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Overview

Motivation

Interaction Nets

Interaction Automata

Related Systems

Conclusion

Motivation

Parallel programming is hard

- ▶ and **load distribution and balancing**
- ▶ data migration costs are often not taken into account, but are relevant on hardware or in a distributed setting
- ▶ desirable: automatic **parallelization** and **load distribution**

Interaction nets as candidate computation model

- ▶ parallel and asynchronous reduction
- ▶ **interaction automata** as refinement for hardware-like execution
- ▶ proof-of-concept implementation: **ia2d**

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Interaction Nets

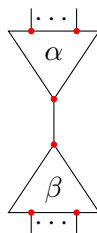
Interaction Automata

Related Systems

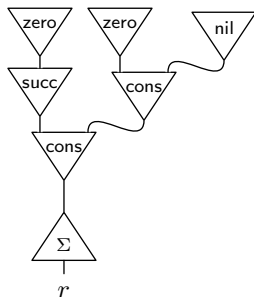
Conclusion

Interaction Nets

- ▶ computation model based on graph-rewriting
- ▶ an **interaction net** is a graph, vertices are called **agents**
- ▶ agents are labeled with **symbols**
- ▶ edges are called **wires**
- ▶ each node has a **principal port** as well as a number of **auxiliary ports** that is fixed for a given symbol
- ▶ when two agents are connected on their principal ports they are called an **active pair**
- ▶ unconnected ports are called **free**
- ▶ the set of free ports is called the **interface**



Example: Interaction Net



- ▶ list constructors: $\text{cons}/2$, $\text{nil}/1$
- ▶ nat constructors: $\text{succ}/1$, $\text{zero}/0$
- ▶ sum operation: $\text{sum}/1$

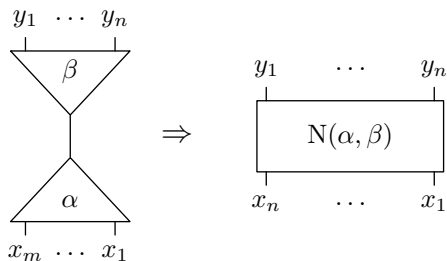
Interaction Net System

An **interaction net system** is a pair (S, R)

- ▶ S set of **symbols** (with fixed arity)
- ▶ R set of **rewrite rules**

Agents and rules are used to encode **data** and **operations**

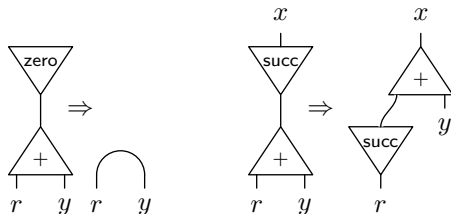
Rules



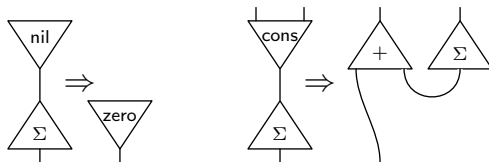
- ▶ left-hand side is an **active pair** of agents
- ▶ right-hand must **preserve interface**
- ▶ at most **one rule per active pair**
- ▶ if $\alpha = \beta$ the right-hand side must be **top-down symmetric**

Example: Rules

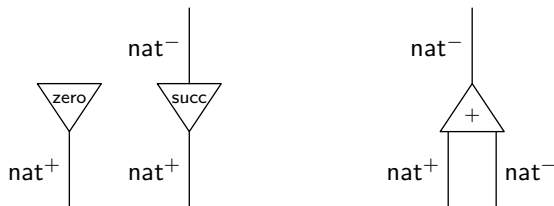
Rules for **adding numbers** in unary encoding:



Rules for **summing up lists of numbers**:

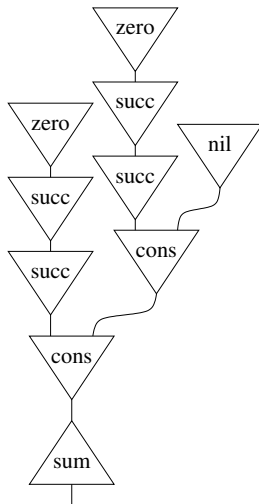


Typed Interaction Nets

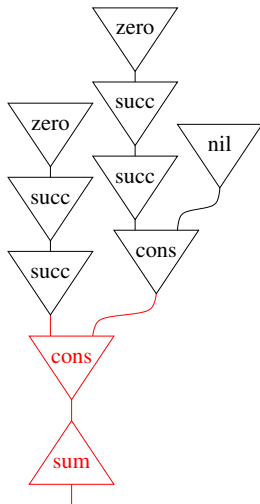


- ▶ typing ports **avoids ill-formed nets**
- ▶ clearly distinguishes **constructors** from **destructors**

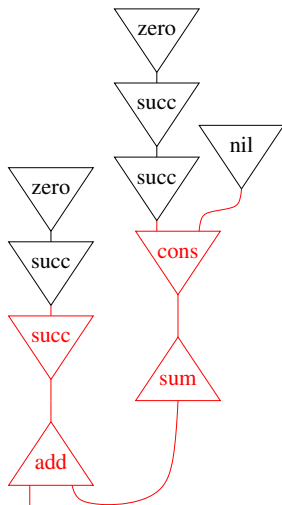
Example IN Computation



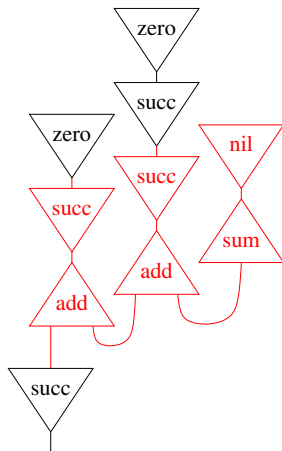
Example IN Computation



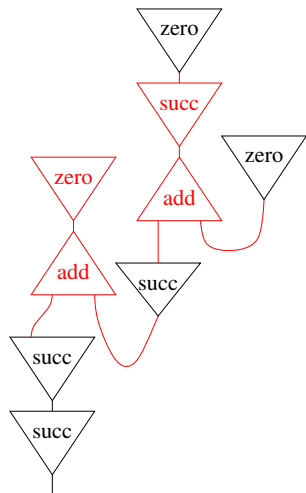
Example IN Computation



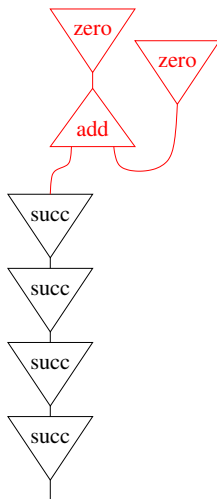
Example IN Computation



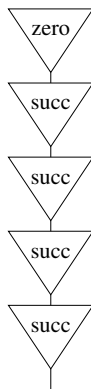
Example IN Computation



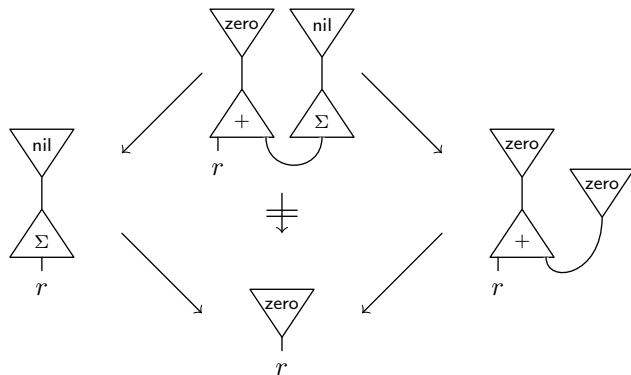
Example IN Computation



Example IN Computation



Properties



- ▶ **linearity**: preserves interface
- ▶ **binary interaction**
- ▶ **no ambiguity**
- ▶ \rightarrow **parallel/asynchronous reduction**

Drawbacks

Some algorithms cannot be represented in interaction net systems

Example: parallel or

$$\begin{aligned} \text{por}(\text{True}, y) &\rightarrow \text{True} \\ \text{por}(x, \text{True}) &\rightarrow \text{True} \\ \text{por}(\text{False}, \text{False}) &\rightarrow \text{False} \end{aligned}$$

Solution:

- ▶ introduce special **amb** agent
- ▶ interaction nets with **multiple principal ports**
- ▶ \rightarrow **non-determinism**

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Interaction Automata

An interaction automaton \mathcal{A} is a quadruple

$$\mathcal{A} = (L, \nu, S, \rightarrow)$$

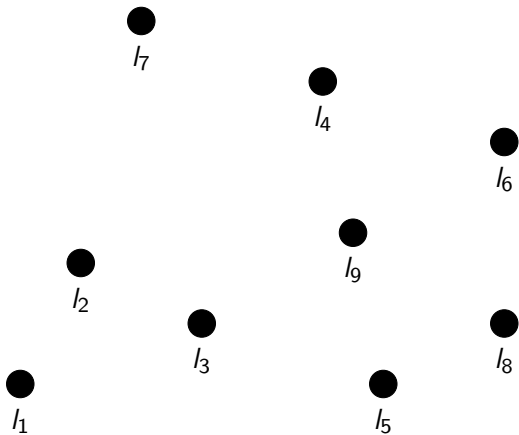
where

L set of locations

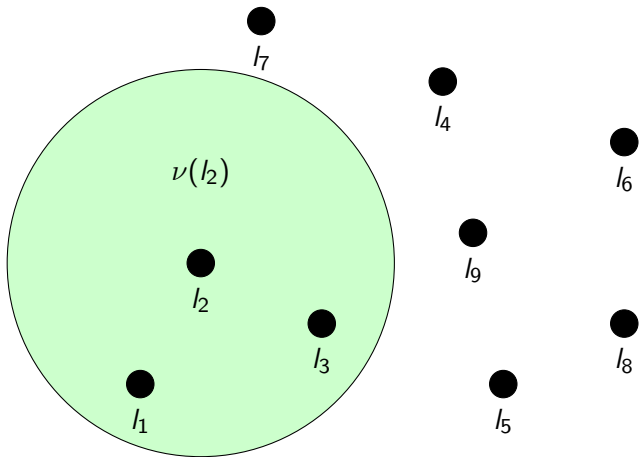
ν neighborhood

S set of symbols

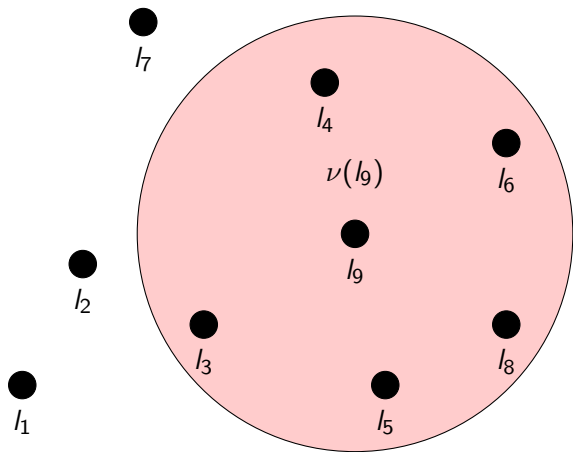
\rightarrow abstract transition



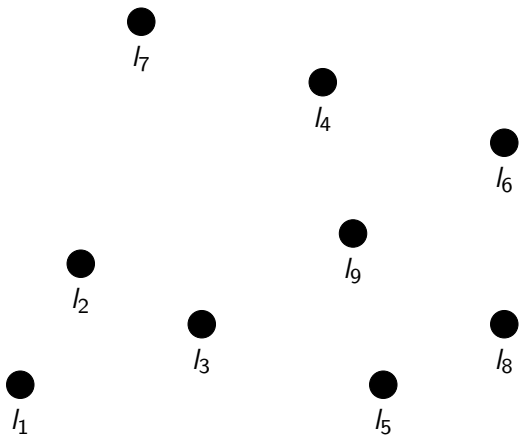
Set of locations L



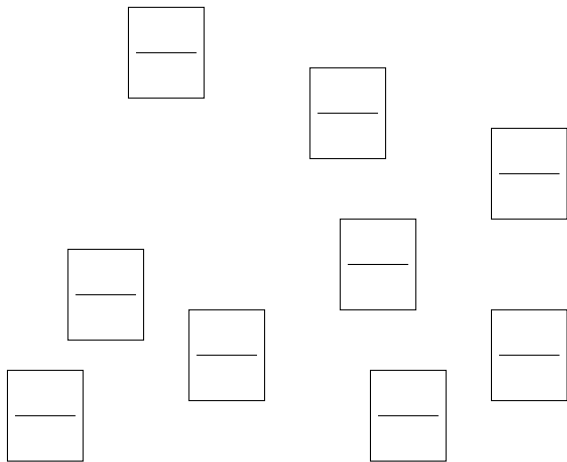
A neighborhood $\nu(l_i)$ is associated to each location



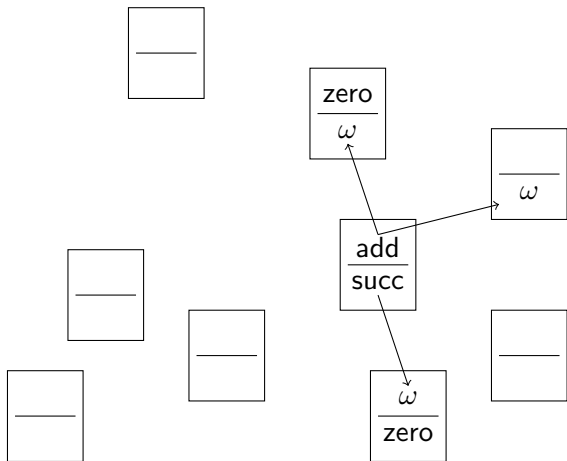
A neighborhood $\nu(l_i)$ is associated to each location



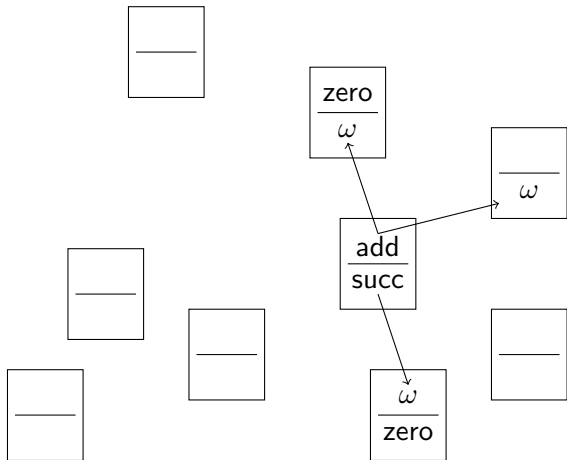
Each location may contain 0, 1 or 2 nodes



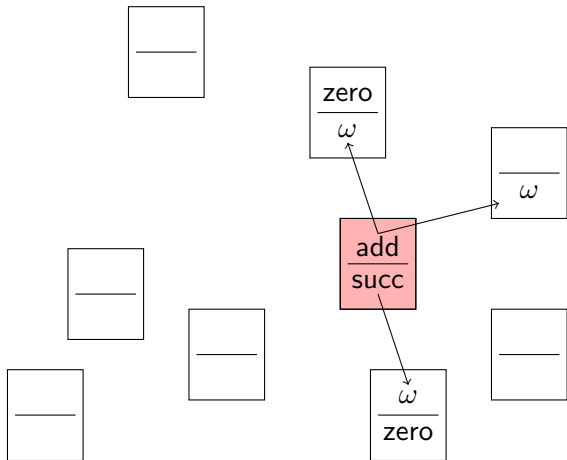
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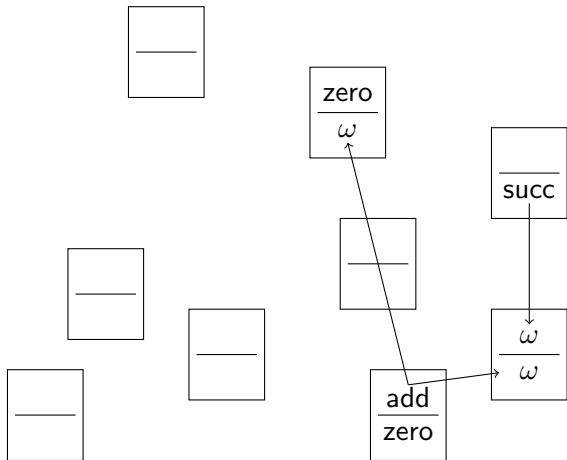
A node is either $s(l_1, \dots, l_k)$, $s \in S$ where k is the arity of s , or ω



Abstract transition (\rightarrow) between configurations



Abstract transition (\rightarrow) between configurations



Abstract transition (\rightarrow) between configurations

Interaction Automata

Properties

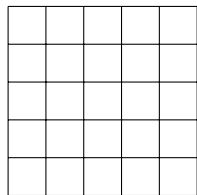
- ▶ reduction is performed locally
- ▶ allocated cells can be placed freely within the neighborhood
- ▶ the allocation strategy is unspecified in the model

ia2d interpreter

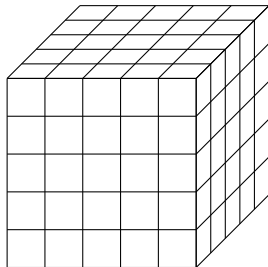
- ▶ proof-of-concept implementation with simple local allocation strategy
- ▶ the locations are layed out in a 2-dimensional grid
→ in some sense close to hardware

Demo

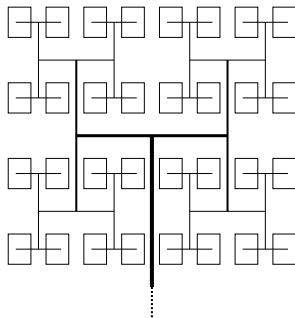
Memory Topologies



grid



cube



nested

Simple Input Language

- ▶ takes care of **non-linear functions**, i.e. erasure and duplication
- ▶ supports **anonymous and higher-order functions**
- ▶ implemented as **source-to-source compiler**

Example

Haskell

```
foldr f z Nil           = z
foldr f z (Cons x xs) = f x (foldr f z xs)
```

Simple input language

```
foldr(Nil(),      f,z) = z;
foldr(Cons(x,xs),f,z) = f(x,foldr(xs,f,z));
```

Practical Considerations

- ▶ limit number of rules
 - **interaction combinators**: 3 symbols and 6 rules suffice to encode arbitrary interaction net systems
- ▶ standard arithmetic
 - special **agents that reference value or computation**
- ▶ input and output
 - **singleton IO agent** and corresponding operations

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Related Systems: Cellular Automata

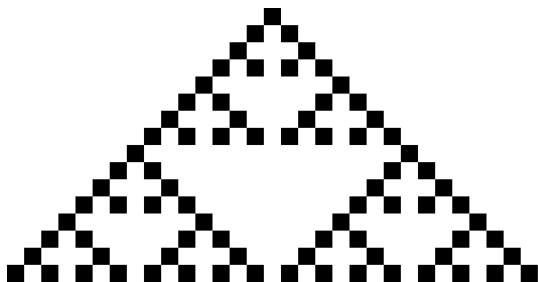
- ▶ interaction automata share some similarities with cellular automata (CA)
- ▶ $\sigma_i(t)$ state of cell at position i at time t
- ▶ transition function Φ

$$\sigma_i(t+1) = \Phi(\sigma_{i-r}(t), \sigma_{i-r+1}(t), \dots, \sigma_{i+r-1}(t), \sigma_{i+r}(t))$$

depends on state of neighbors

- ▶ CA are synchronous parallel computational model
- ▶ Turing-complete: Rule 110

Example: Rule 90



---	--#	-#-	-##	#--	#_#	##-	###
-	#	-	#	#	-	#	-
0	1	2	3	4	5	6	7

Related Systems

Open Multi-Processing (OpenMP)

- ▶ shared memory
- ▶ parallelization by adding annotations (pragmas)

Message Passing Interface (MPI)

- ▶ distributed memory
- ▶ complete restructuring of program

Open Compute Language (OpenCL)

- ▶ run small kernels on many CPUs (GPUs)
- ▶ program-in-program

Related Systems: Data-Flow Graph

Fresh Breeze

- ▶ data-flow processor
- ▶ graph of tasks

Parallel Haskell

- ▶ `Par` monad

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Proof-of-concept implementation

- ▶ **parallel computation** model with **localized reduction** on top of memory scheme with **limited connectivity** and **locally bounded storage**
- ▶ functional programs can be run on this model
- ▶ the implementation shows that reasonable memory management strategies can be implemented locally

Limits

- ▶ grid layout is **limiting**
- ▶ **nested layout scheme** seems more promising