

# SAT and SMT Solving

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# Outline

- Summary of Last Week
- Unsatisfiable Cores
- Application: FPGA Routing
- Algorithm by Fu and Malik
- Unsatisfiable Cores in Practice

# Maximum Satisfiability

Consider CNF formulas  $\chi$  and  $\varphi$  as sets of clauses such that  $\chi$  is satisfiable.

## Definitions

- ▶  $\text{maxSAT}(\varphi)$  is maximal  $\sum_{C \in \varphi} \bar{v}(C)$  for valuation  $v$
- ▶  $\text{pmaxSAT}(\varphi, \chi)$  is maximal  $\sum_{C \in \varphi} \bar{v}(C)$  for valuation  $v$  with  $v(\chi) = \text{T}$

## Definitions

given weights  $w_C \in \mathbb{Z}$  for all  $C \in \varphi$ ,

- ▶  $\text{maxSAT}_w(\varphi)$  is maximal  $\sum_{C \in \varphi} w_C \cdot \bar{v}(C)$  for valuation  $v$ ?
- ▶  $\text{pmaxSAT}_w(\varphi, \chi)$  is maximal  $\sum_{C \in \varphi} w_C \cdot \bar{v}(C)$  for valuation  $v$  with  $v(\chi) = \text{T}$

## Definition

$\text{minUNSAT}(\varphi)$  is minimal  $\sum_{C \in \varphi} \bar{v}(\neg C)$  for valuation  $v$

## Lemma

$$|\varphi| = |\text{minUNSAT}(\varphi)| + |\text{maxSAT}(\varphi)|$$

# Branch & Bound

## Idea

- ▶ gets list of clauses  $\varphi$  as input return  $\text{minUNSAT}(\varphi)$
- ▶ explores assignments in depth-first search

---

```
function BnB( $\varphi$ , UB)
   $\varphi$  = simp( $\varphi$ )
  if  $\varphi$  contains only empty clauses then
    return #empty( $\varphi$ )
  if #empty( $\varphi$ )  $\geq$  UB then
    return UB
  x = selectVariable( $\varphi$ )
  UB' = min(UB, BnB( $\varphi_x$ , UB))
  return min(UB', BnB( $\varphi_{\bar{x}}$ , UB'))
```

---

## Theorem

$$\text{BnB}(\varphi, |\varphi|) = \text{minUNSAT}(\varphi)$$

# Binary Search

## Idea

- ▶ gets list of clauses  $\varphi$  as input and returns  $\text{minUNSAT}(\varphi)$
- ▶ repeatedly call SAT solver in binary search fashion

## Definitions

- ▶ **cardinality constraint** is

$$\sum_{x \in X} x \bowtie N$$

where  $\bowtie$  is  $=$ ,  $<$ ,  $>$ ,  $\leq$ , or  $\geq$ ,  $X$  is set of propositional variables, and  $N \in \mathbb{N}$

- ▶ valuation  $v$  satisfies  $\sum_{x \in X} x \bowtie N$  iff  $k \bowtie N$   
where  $k$  is number of variables  $x \in X$  such that  $v(x) = \text{T}$

## Remark

cardinality constraints are expressible in CNF

## Algorithm (Binary Search)

---

```
function BinarySearch( $\{C_1, \dots, C_m\}$ )  
   $\varphi := \{C_1 \vee b_1, \dots, C_m \vee b_m\}$   
  return search( $\varphi, 0, m$ )
```

$b_1, \dots, b_m$  are fresh variables

```
function search( $\varphi, L, U$ )  
  if  $L \geq U$  then  
    return U  
   $\text{mid} := \lfloor \frac{U+L}{2} \rfloor$   
  if SAT( $\varphi \wedge \text{CNF}(\sum_{i=1}^m b_i \leq \text{mid})$ ) then  
    return search( $\varphi, L, \text{mid}$ )  
  else  
    return search( $\varphi, \text{mid} + 1, U$ )
```

---

## Theorem

$\text{BinarySearch}(\psi) = \min \text{UNSAT}(\psi)$

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for unsatisfiable CNF formula  $\varphi$  given as set of clauses

- ▶ unsatisfiable core (UC) of  $\varphi$  is  $\psi \subseteq \varphi$  such that  $\bigwedge_{C \in \psi} C$  is unsatisfiable



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$$\varphi = \{\neg x, \quad x \vee z, \quad \neg y \vee \neg z, \quad x, \quad y \vee \neg z\}$$

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unsatisfiable cores are

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- ▶  $\{\neg x, x \vee z, \neg y \vee \neg z, y \vee \neg z\}$  minimal
- ▶  $\{\neg x, x\}$  minimal and SUC

## Remark

SUC is always minimal unsatisfiable core

## Example

$\varphi = \{C_1, \dots, C_6\}$  is unsatisfiable

$$C_1: x_1 \vee \neg x_3$$

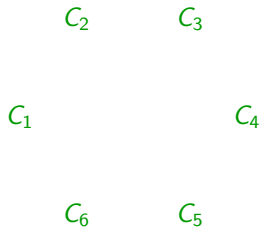
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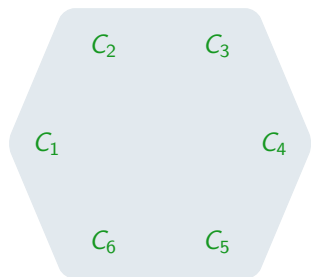
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$\varphi$  has 9 unsatisfiable cores:



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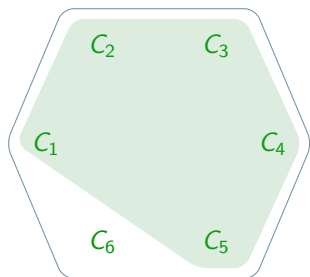
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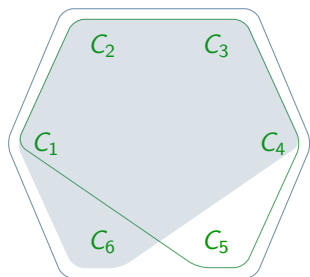
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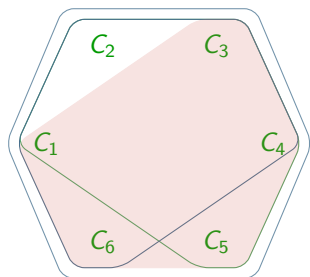
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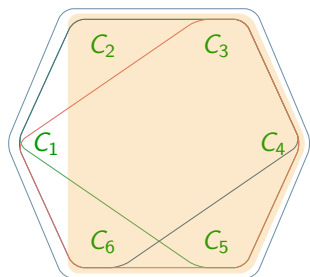
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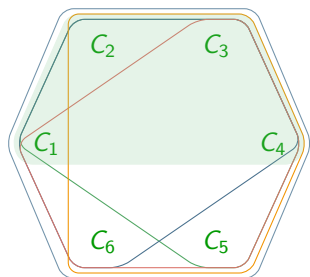
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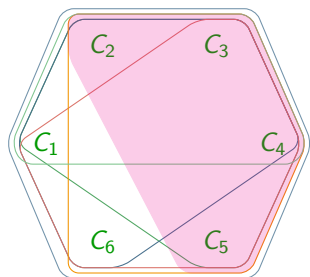
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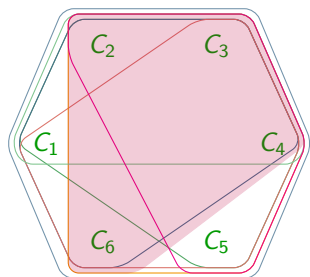
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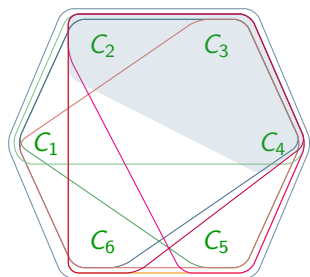
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## Idea

- ▶ repeatedly pick clause  $C$  from  $\varphi$  and check satisfiability:  
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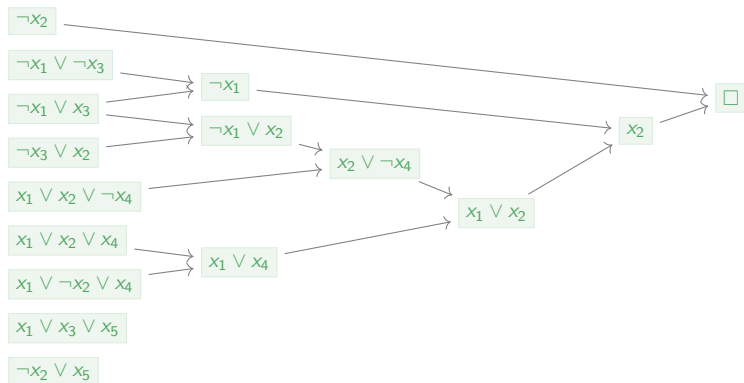
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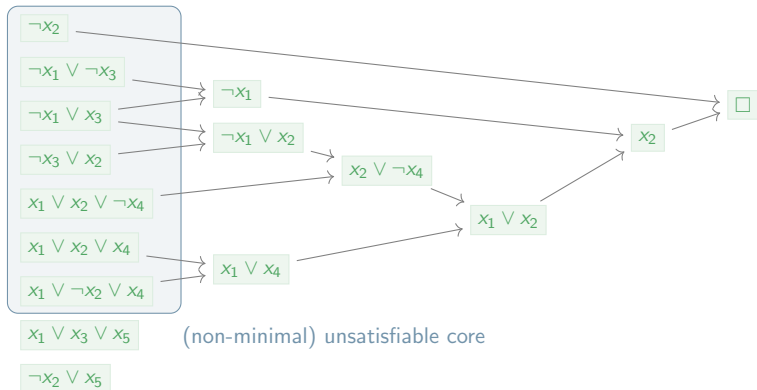


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## Example (Resolution Graph)



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directed acyclic graph  $G = (V, E)$  is **resolution graph** for set of clauses  $\varphi$



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- 4  $\forall C \in V_c \exists$  edges  $D \rightarrow C, D' \rightarrow C$  such that  $C$  is resolvent of  $D$  and  $D'$ , and

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## Remark

- ▶ if  $\varphi$  is unsatisfiable then sequence of resolution steps can derive  $\square$   
because resolution is **complete** proof method
- ▶ so resolution graph exists

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## Definition (Resolution Graph)

directed acyclic graph  $G = (V, E)$  is resolution graph for set of clauses  $\varphi$  if

- 1  $V = V_i \uplus V_c$  is set of clauses and  $V_i = \varphi$ ,
- 2  $V_i$  nodes have no incoming edges, initial nodes
- 3 there is exactly one node  $\square$  without outgoing edges,
- 4  $\forall C \in V_c \exists$  edges  $D \rightarrow C, D' \rightarrow C$  such that  $C$  is resolvent of  $D$  and  $D'$ , and
- 5 there are no other edges.

## Remark

- ▶ if  $\varphi$  is unsatisfiable then sequence of resolution steps can derive  $\square$   
because resolution is complete proof method
- ▶ so resolution graph exists

## Notation

- ▶  $Reach_G(C)$  is set of nodes reachable from  $C$  in  $G$

Assume  $\varphi$  is unsatisfiable.

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- ▶  $Reach_G^E(C)$  is set of edges reachable from  $C$  in  $G$
- ▶  $\overline{N}$  is  $V \setminus N$  for any set of nodes  $N$

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**Algorithm**  $\text{minUnsatCore}(\varphi)$ 

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**Input:**        **unsatisfiable formula**  $\varphi$

**Output:**      minimal unsatisfiable core of  $\varphi$

build resolution graph  $G = (V_i \uplus V_c, E)$  for  $\varphi$

**while**  $\exists$  unmarked clause in  $V_i$  **do**

$C \leftarrow$  unmarked clause in  $V_i$

**if**  $\text{SAT}(\overline{\text{Reach}_G(C)})$  **then**

        mark  $C$

$\triangleright$  subgraph without  $C$  satisfiable?

$\triangleright C$  is UC member

**else**

        build resolution graph  $G' = (V'_i \uplus V'_c, E')$  for  $\overline{\text{Reach}_G(C)}$

$V_i \leftarrow V_i \setminus \{C\}$  and  $V_c \leftarrow V'_c \cup (V_c \setminus \text{Reach}_G(C))$

$E \leftarrow E' \cup (E \setminus \text{Reach}_G^E(C))$

$G \leftarrow (V_i \cup V_c, E)$

$G \leftarrow G|_{\square}$

$\triangleright$  restrict to nodes with path to  $\square$

return  $V_i$

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**Algorithm**  $\text{minUnsatCore}(\varphi)$ 

---

**Input:** unsatisfiable formula  $\varphi$

**Output:** minimal unsatisfiable core of  $\varphi$

build resolution graph  $G = (V_i \uplus V_c, E)$  for  $\varphi$

**while**  $\exists$  unmarked clause in  $V_i$  **do**

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$G \leftarrow (V_i \cup V_c, E)$

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$\triangleright$  restrict to nodes with path to  $\square$

return  $V_i$

---

---

**Algorithm**  $\text{minUnsatCore}(\varphi)$ 

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**Algorithm**  $\text{minUnsatCore}(\varphi)$ 

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**Algorithm**  $\text{minUnsatCore}(\varphi)$ 

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**Algorithm**  $\text{minUnsatCore}(\varphi)$ 

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$G \leftarrow (V_i \cup V_c, E)$

$G \leftarrow G|_{\square}$

$\triangleright$  restrict to nodes with path to  $\square$

return  $V_i$

---



---

**Algorithm**  $\text{minUnsatCore}(\varphi)$ 

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**if**  $\text{SAT}(\overline{\text{Reach}_G(C)})$  **then**

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$\triangleright$  subgraph without  $C$  satisfiable?

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return  $V_i$

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$\triangleright$  restrict to nodes with path to  $\square$

return  $V_i$

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**while**  $\exists$  unmarked clause in  $V_i$  **do**

$C \leftarrow$  unmarked clause in  $V_i$

**if**  $\text{SAT}(\overline{\text{Reach}_G(C)})$  **then**

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$E \leftarrow E' \cup (E \setminus \text{Reach}_G^E(C))$

$G \leftarrow (V_i \cup V_c, E)$

$G \leftarrow G|_{\square}$

$\triangleright$  restrict to nodes with path to  $\square$

return  $V_i$

---

---

**Algorithm** minUnsatCore( $\varphi$ )

---

**Input:** unsatisfiable formula  $\varphi$

**Output:** minimal unsatisfiable core of  $\varphi$

build resolution graph  $G = (V_i \uplus V_c, E)$  for  $\varphi$

**while**  $\exists$  unmarked clause in  $V_i$  **do**

$C \leftarrow$  unmarked clause in  $V_i$

**if**  $\text{SAT}(\overline{\text{Reach}_G(C)})$  **then**

        mark  $C$

$\triangleright$  subgraph without  $C$  satisfiable?

$\triangleright C$  is UC member

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$V_i \leftarrow V_i \setminus \{C\}$  and  $V_c \leftarrow V'_c \cup (V_c \setminus \text{Reach}_G(C))$

$E \leftarrow E' \cup (E \setminus \text{Reach}_G^E(C))$

$G \leftarrow (V_i \cup V_c, E)$

$G \leftarrow G|_{\square}$

$\triangleright$  restrict to nodes with path to  $\square$

return  $V_i$

---

---

**Algorithm** minUnsatCore( $\varphi$ )

---

**Input:** unsatisfiable formula  $\varphi$

**Output:** minimal unsatisfiable core of  $\varphi$

build resolution graph  $G = (V_i \uplus V_c, E)$  for  $\varphi$

**while**  $\exists$  unmarked clause in  $V_i$  **do**

$C \leftarrow$  unmarked clause in  $V_i$

**if**  $\text{SAT}(\overline{\text{Reach}_G(C)})$  **then**

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$G \leftarrow G|_{\square}$

$\triangleright$  restrict to nodes with path to  $\square$

return  $V_i$

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---

**Algorithm** minUnsatCore( $\varphi$ )

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**Input:** unsatisfiable formula  $\varphi$

**Output:** minimal unsatisfiable core of  $\varphi$

build resolution graph  $G = (V_i \uplus V_c, E)$  for  $\varphi$

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**if**  $\text{SAT}(\overline{\text{Reach}_G(C)})$  **then**

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$\triangleright$  subgraph without  $C$  satisfiable?

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$G \leftarrow (V_i \cup V_c, E)$

$G \leftarrow G|_{\square}$

$\triangleright$  restrict to nodes with path to  $\square$

return  $V_i$

---

---

**Algorithm**  $\text{minUnsatCore}(\varphi)$ 

---

**Input:** unsatisfiable formula  $\varphi$

**Output:** minimal unsatisfiable core of  $\varphi$

build resolution graph  $G = (V_i \uplus V_c, E)$  for  $\varphi$

**while**  $\exists$  unmarked clause in  $V_i$  **do**

$C \leftarrow$  unmarked clause in  $V_i$

**if**  $\text{SAT}(\overline{\text{Reach}_G(C)})$  **then**

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$G \leftarrow (V_i \cup V_c, E)$

$G \leftarrow G|_{\square}$

$\triangleright$  restrict to nodes with path to  $\square$

return  $V_i$

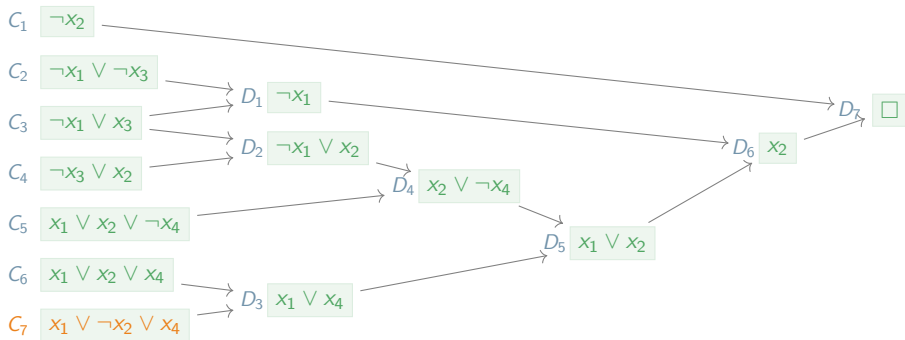
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## Theorem

*if  $\varphi$  unsatisfiable then  $\text{minUnsatCore}(\varphi)$  is minimal unsatisfiable core of  $\varphi$*



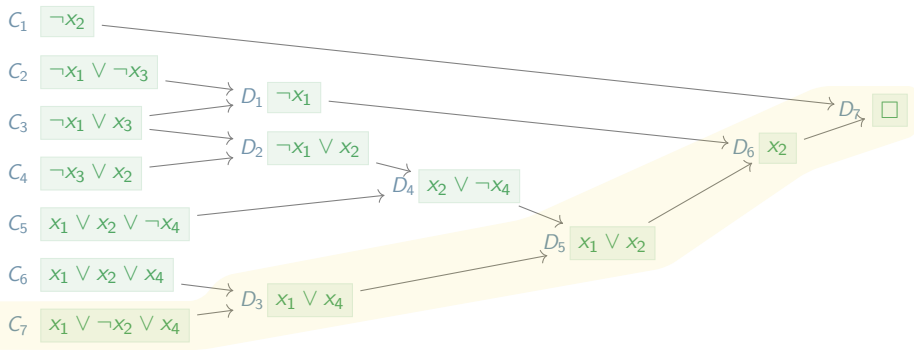
## Example



**minUnsatCore( $\varphi$ )**

- pick  $C_7$

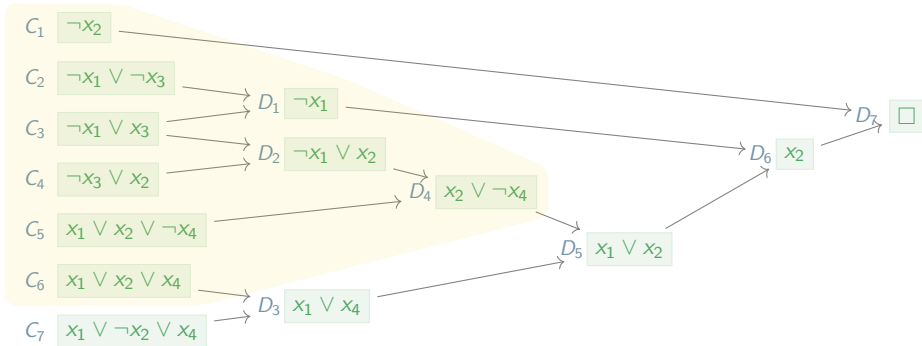
## Example



## $\text{minUnsatCore}(\varphi)$

- pick  $C_7$
- $\text{Reach}_G(C_7) = \{C_7, D_3, D_5, D_6, D_7\}$

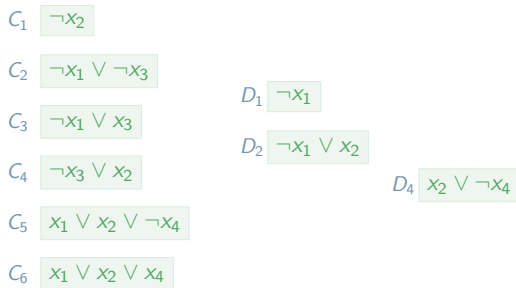
## Example



## $\text{minUnsatCore}(\varphi)$

- pick  $C_7$
- $\text{Reach}_G(C_7) = \{C_7, D_3, D_5, D_6, D_7\}$      $\overline{\text{Reach}_G(C_7)} = \{C_1, \dots, C_6, D_1, D_2, D_4\}$

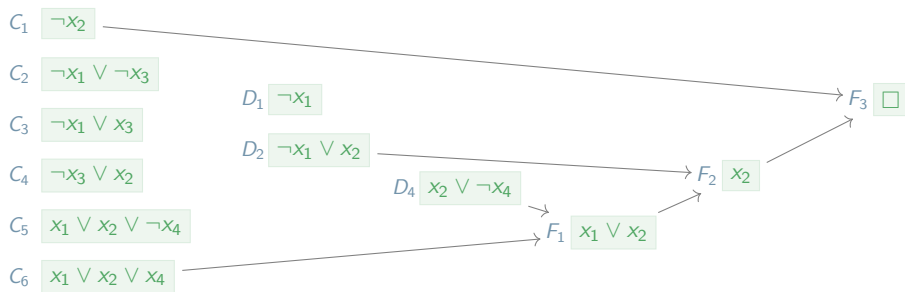
## Example



## $\text{minUnsatCore}(\varphi)$

- pick  $C_7$
- $\text{Reach}_G(C_7) = \{C_7, D_3, D_5, D_6, D_7\}$       $\overline{\text{Reach}_G(C_7)} = \{C_1, \dots, C_6, D_1, D_2, D_4\}$
- check  $\text{SAT}(\overline{\text{Reach}_G(C_7)})$

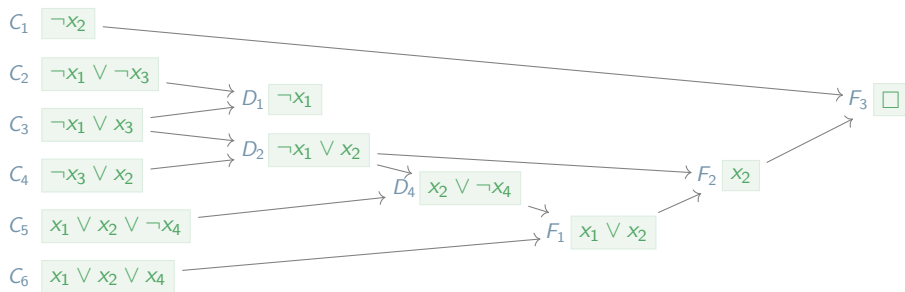
## Example



## $\text{minUnsatCore}(\varphi)$

- pick  $C_7$
- $\text{Reach}_G(C_7) = \{C_7, D_3, D_5, D_6, D_7\}$      $\overline{\text{Reach}_G(C_7)} = \{C_1, \dots, C_6, D_1, D_2, D_4\}$
- check  $\text{SAT}(\overline{\text{Reach}_G(C_7)})$
- unsatisfiable: get new resolution graph  $G_7$  for  $\varphi \cup \{D_1, D_2, D_4\}$

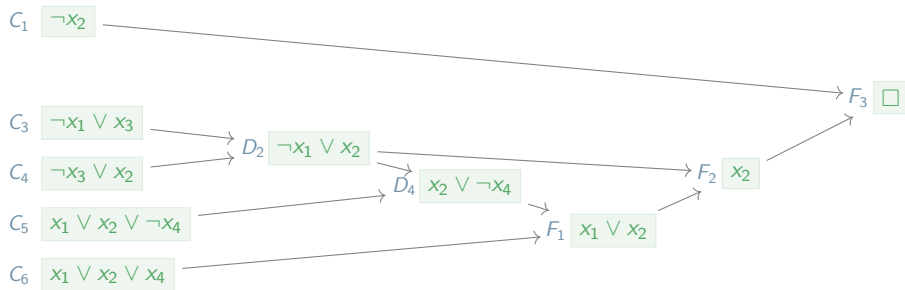
## Example



## $\text{minUnsatCore}(\varphi)$

- pick  $C_7$
- $\text{Reach}_G(C_7) = \{C_7, D_3, D_5, D_6, D_7\}$      $\overline{\text{Reach}_G(C_7)} = \{C_1, \dots, C_6, D_1, D_2, D_4\}$
- check  $\text{SAT}(\overline{\text{Reach}_G(C_7)})$
- unsatisfiable: get new resolution graph  $G_7$  for  $\varphi \cup \{D_1, D_2, D_4\}$
- construct resolution graph  $G'$  for  $\varphi$  by adding edges from  $G$  to  $G_7$

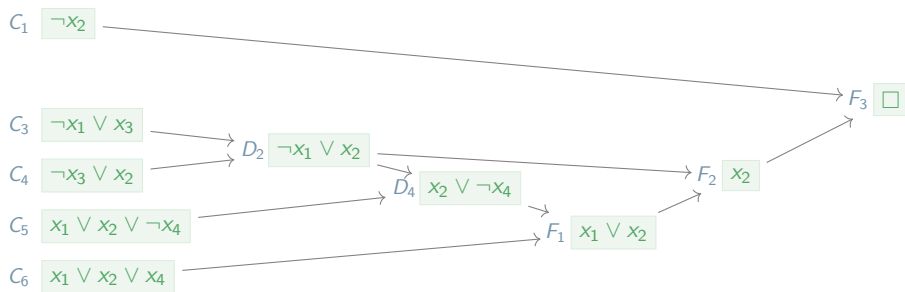
## Example



## minUnsatCore( $\varphi$ )

- pick  $C_7$
- $Reach_G(C_7) = \{C_7, D_3, D_5, D_6, D_7\}$      $\overline{Reach_G(C_7)} = \{C_1, \dots, C_6, D_1, D_2, D_4\}$
- check  $SAT(\overline{Reach_G(C_7)})$
- unsatisfiable: get new resolution graph  $G_7$  for  $\varphi \cup \{D_1, D_2, D_4\}$
- construct resolution graph  $G'$  for  $\varphi$  by adding edges from  $G$  to  $G_7$
- set  $G$  to  $G'$  restricted to nodes with path to  $\square$

## Example

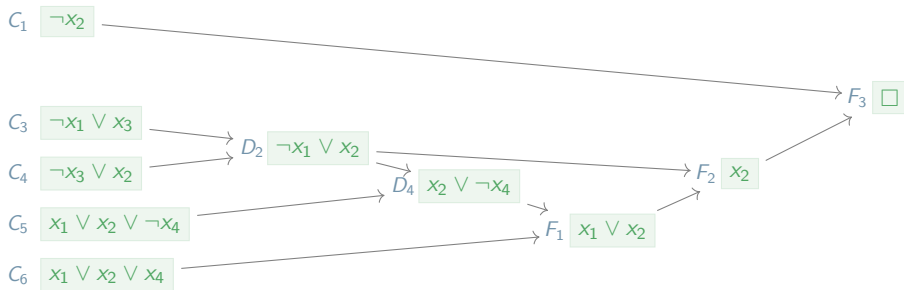


## $\text{minUnsatCore}(\varphi)$

- pick  $C_7$
- $\text{Reach}_G(C_7) = \{C_7, D_3, D_5, D_6, D_7\}$      $\overline{\text{Reach}_G(C_7)} = \{C_1, \dots, C_6, D_1, D_2, D_4\}$
- check  $\text{SAT}(\overline{\text{Reach}_G(C_7)})$
- unsatisfiable: get new resolution graph  $G_7$  for  $\varphi \cup \{D_1, D_2, D_4\}$
- construct resolution graph  $G'$  for  $\varphi$  by adding edges from  $G$  to  $G_7$
- set  $G$  to  $G'$  restricted to nodes with path to  $\square$
- after 5 more loop iterations: return  $\{C_1, C_3, \dots, C_6\}$



## Example



## minUnsatCore( $\varphi$ )

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re-use relevant resolvents:  
fewer steps to  $\square$

# Application: FPGA Routing

## Field Programmable Gate Arrays (FPGAs)

- ▶ can simulate microprocessors but faster for special tasks (from complex combinatorics to mere logic)



# Application: FPGA Routing

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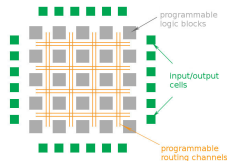
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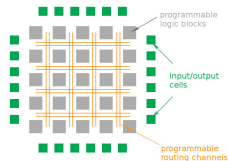
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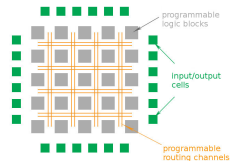
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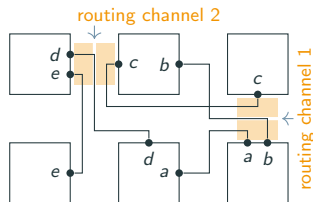
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## Example (Encoding Routing Requirements)

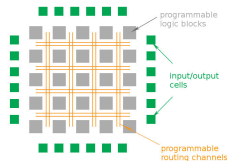
- ▶ consider connections  $a, b, c, d, e$  of 2 bits each



# Application: FPGA Routing

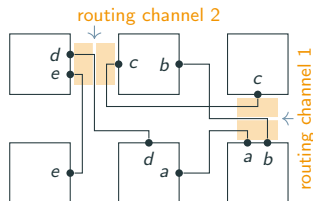
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$$a_0 \vee a_1$$

$$b_0 \vee b_1$$

$$c_0 \vee c_1$$

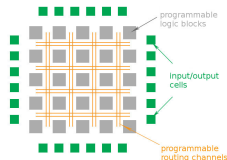
$$d_0 \vee d_1$$

$$e_0 \vee e_1$$

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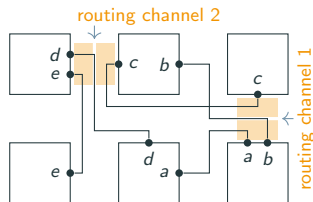
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- ▶ consider connections  $a, b, c, d, e$  of 2 bits each
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- ▶ **exclusivity**: each channel has only 2 tracks



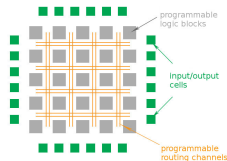
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$b_0 \vee b_1$	$\neg a_0 \vee \neg c_0$	$\neg c_0 \vee \neg e_0$
$c_0 \vee c_1$	$\neg b_0 \vee \neg c_0$	$\neg d_0 \vee \neg e_0$
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# Application: FPGA Routing

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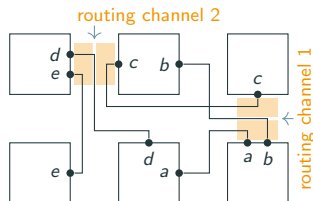
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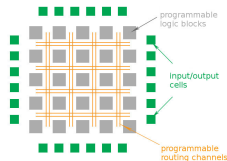
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# Application: FPGA Routing

## Field Programmable Gate Arrays (FPGAs)

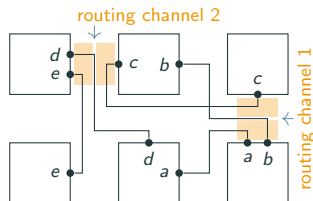
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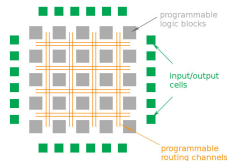


$UC_1$ : channel 1 capacity exceeded

# Application: FPGA Routing

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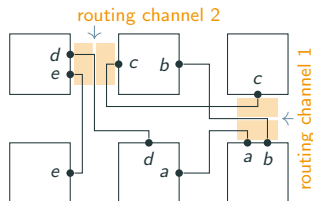
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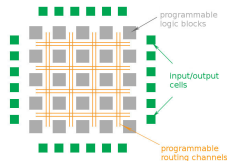
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# Application: FPGA Routing

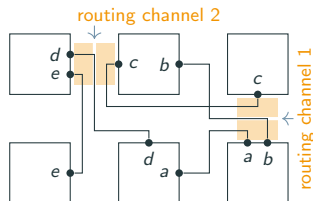
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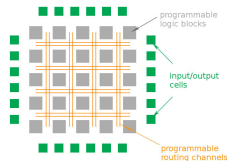
$UC_2$ : channel 2 capacity exceeded

$UC_3$ :  $c$  is overconstrained

# Application: FPGA Routing

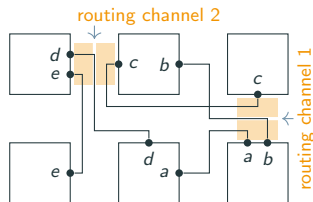
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$$a_0 \vee a_1 \quad \bullet \quad \bullet \quad \bullet \quad \bullet \quad \neg a_0 \vee \neg b_0 \quad \bullet \quad \bullet \quad \neg c_0 \vee \neg d_0 \quad \bullet \quad \bullet$$

$$b_0 \vee b_1 \quad \bullet \quad \bullet \quad \bullet \quad \bullet \quad \neg a_0 \vee \neg c_0 \quad \bullet \quad \bullet \quad \neg c_0 \vee \neg e_0 \quad \bullet \quad \bullet$$

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$UC_1$ : channel 1 capacity exceeded

$UC_2$ : channel 2 capacity exceeded

$UC_3$ :  $c$  is overconstrained

$UC_4$ :  $c$  is overconstrained

- Summary of Last Week
- Unsatisfiable Cores
- Application: FPGA Routing
- Algorithm by Fu and Malik
- Unsatisfiable Cores in Practice

# Bounds for Maximum Satisfiability

consider CNF formula  $\varphi = C_1 \wedge \cdots \wedge C_m$

## Definition

**blocked formula** is  $\varphi_B = (C_1 \vee b_1) \wedge \cdots \wedge (C_m \vee b_m)$  for fresh variables  $b_1, \dots, b_m$

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## Lemma (Lower Bound)

if  $v$  satisfies  $\varphi_B$  and  $B_T = \{b_i \mid v(b_i) = T\}$  then  $\text{maxSAT}(\varphi) \geq m - |B_T|$



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## Example (Upper Bound)

$$\begin{array}{ccccccc} \neg x_3 \vee \neg x_4 & \neg x_3 \vee x_4 & x_7 & & & & \\ & x_3 & & & & & \\ & & \neg x_7 \vee x_8 & \neg x_1 \vee x_8 & & & \\ & & & & x_4 \vee x_5 & x_1 \vee \neg x_5 \vee x_6 & \\ & & & & & x_5 \vee \neg x_6 & \\ x_1 & \neg x_1 \vee \neg x_3 & \neg x_7 \vee \neg x_8 \vee x_6 & \neg x_9 \vee x_2 & & & \\ & & & & \neg x_4 \vee x_5 & \neg x_1 \vee \neg x_5 & \\ \neg x_1 \vee \neg x_2 & \neg x_1 \vee x_2 & \neg x_7 \vee \neg x_8 \vee \neg x_6 & & & & \end{array}$$

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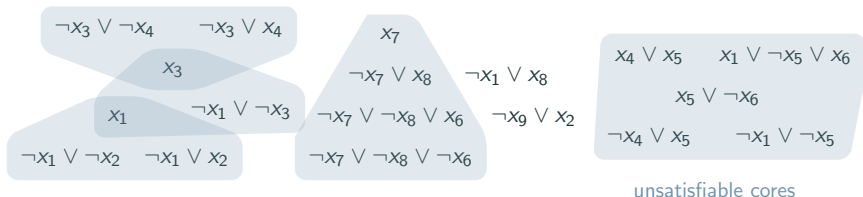
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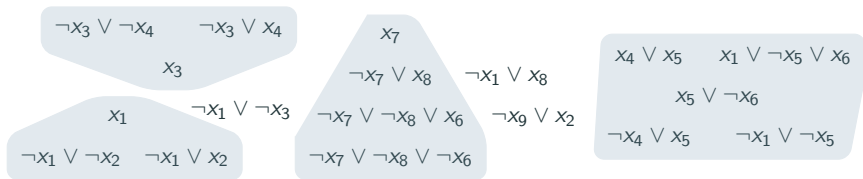
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## Example (Upper Bound)



4 disjoint unsatisfiable cores

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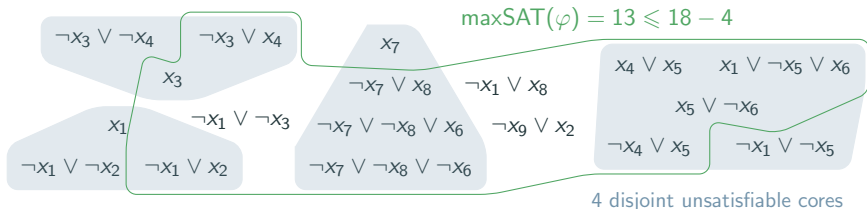
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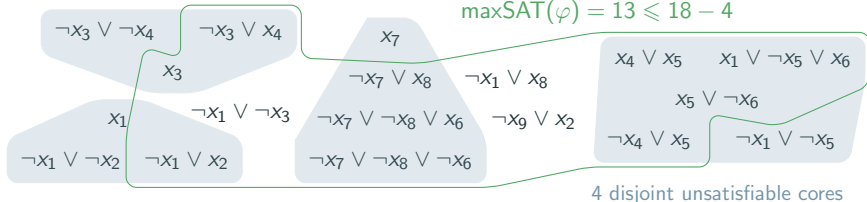
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## Example (Upper Bound)

must miss at least one clause from every core!

$$\text{maxSAT}(\varphi) = 13 \leq 18 - 4$$



## Idea

- ▶ **maxsat** valuation must make at least **one clause** in unsatisfiable core **false**

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- ▶ maxsat valuation must make at least one clause in unsatisfiable core false
- ▶ while there **exists (minimal) unsatisfiable core**:  
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## Definition (Partial minUNSAT)

$\text{pminUNSAT}(\chi, \varphi)$  is minimal  $\sum_{C \in \varphi} \bar{v}(\neg C)$  for valuation  $v$  with  $v(\chi) = \text{T}$

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## Lemma

$$|\varphi| = \text{pminUNSAT}(\chi, \varphi) + \text{pmaxSAT}(\chi, \varphi)$$

## Example

$$\chi: \neg x_1 \vee x_3$$

$$\neg x_7 \vee x_2$$

$$x_7 \vee x_2$$

$$x_1 \vee \neg x_2$$

$$\varphi: \neg x_1 \vee \neg x_2$$

$$\neg x_1 \vee x_2$$

$$\neg x_1 \vee x_7$$

$$x_1$$

$$\neg x_3 \vee x_4$$

$$x_3$$

$$\neg x_3 \vee \neg x_4$$

$$x_4 \vee x_5$$

$$\neg x_4 \vee x_5$$

$$x_1 \vee \neg x_5 \vee x_6$$

$$x_5 \vee \neg x_6$$

$$x_7$$

$$\neg x_7 \vee x_8$$

$$\neg x_7 \vee \neg x_8 \vee x_6$$

$$\neg x_7 \vee \neg x_8 \vee \neg x_6$$

$$\neg x_1 \vee \neg x_3$$

## Example

$\chi:$	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
$\varphi:$	$\neg x_1 \vee \neg x_2$	$\neg x_1 \vee x_2$	$\neg x_1 \vee x_7$	$x_1$
	$\neg x_3 \vee x_4$	$x_3$	$\neg x_3 \vee \neg x_4$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7$
	$\neg x_7 \vee x_8$	$\neg x_7 \vee \neg x_8 \vee x_6$	$\neg x_7 \vee \neg x_8 \vee \neg x_6$	$\neg x_1 \vee \neg x_3$

► unsatisfiable core:  $\neg x_1 \vee \neg x_2$ ,  $\neg x_1 \vee x_2$ ,  $x_1$

## Example

$\chi:$	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
$\varphi:$	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4$	$x_3$	$\neg x_3 \vee \neg x_4$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7$
	$\neg x_7 \vee x_8$	$\neg x_7 \vee \neg x_8 \vee x_6$	$\neg x_7 \vee \neg x_8 \vee \neg x_6$	$\neg x_1 \vee \neg x_3$

- unsatisfiable core:  $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$   
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$   
 $\text{cost} = 1$

## Example

$\chi:$	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
$\varphi:$	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4$	$x_3$	$\neg x_3 \vee \neg x_4$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7$
	$\neg x_7 \vee x_8$	$\neg x_7 \vee \neg x_8 \vee x_6$	$\neg x_7 \vee \neg x_8 \vee \neg x_6$	$\neg x_1 \vee \neg x_3$

- unsatisfiable core:  $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$   
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$   
 $\text{cost} = 1$
- unsatisfiable core:  $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$

## Example

$\chi$ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
$\varphi$ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7$
	$\neg x_7 \vee x_8$	$\neg x_7 \vee \neg x_8 \vee x_6$	$\neg x_7 \vee \neg x_8 \vee \neg x_6$	$\neg x_1 \vee \neg x_3$

- ▶ unsatisfiable core:  $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$   
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$   
 $\text{cost} = 1$
- ▶ unsatisfiable core:  $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$   
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$   
 $\text{cost} = 2$



## Example

$\chi$ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
$\varphi$ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7$
	$\neg x_7 \vee x_8$	$\neg x_7 \vee \neg x_8 \vee x_6$	$\neg x_7 \vee \neg x_8 \vee \neg x_6$	$\neg x_1 \vee \neg x_3$

- ▶ unsatisfiable core:  $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$   
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$   
 $\text{cost} = 1$
- ▶ unsatisfiable core:  $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$   
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$   
 $\text{cost} = 2$
- ▶ unsatisfiable core:  $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$

## Example

$\chi$ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
$\varphi$ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7 \vee d_1$
	$\neg x_7 \vee x_8 \vee d_2$	$\neg x_7 \vee \neg x_8 \vee x_6 \vee d_3$	$\neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4$	$\neg x_1 \vee \neg x_3$

- ▶ unsatisfiable core:  $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$   
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$   
 $\text{cost} = 1$
- ▶ unsatisfiable core:  $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$   
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$   
 $\text{cost} = 2$
- ▶ unsatisfiable core:  $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$   
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$   
 $\text{cost} = 3$

## Example

$\chi$ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
$\varphi$ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7 \vee d_1$
	$\neg x_7 \vee x_8 \vee d_2$	$\neg x_7 \vee \neg x_8 \vee x_6 \vee d_3$	$\neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4$	$\neg x_1 \vee \neg x_3$

- ▶ unsatisfiable core:  $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$   
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$   
 $\text{cost} = 1$
- ▶ unsatisfiable core:  $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$   
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$   
 $\text{cost} = 2$
- ▶ unsatisfiable core:  $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$   
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$   
 $\text{cost} = 3$
- ▶ unsatisfiable core:  $\neg x_1 \vee x_3, \neg x_7 \vee x_2, x_7 \vee x_2, x_1 \vee \neg x_2, \neg x_1 \vee \neg x_3$

## Example

$\chi$ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
$\varphi$ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7 \vee d_1$
	$\neg x_7 \vee x_8 \vee d_2$	$\neg x_7 \vee \neg x_8 \vee x_6 \vee d_3$	$\neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4$	$\neg x_1 \vee \neg x_3 \vee e_1$

- unsatisfiable core:  $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$   
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$   
 $\text{cost} = 1$
- unsatisfiable core:  $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$   
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$   
 $\text{cost} = 2$
- unsatisfiable core:  $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$   
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$   
 $\text{cost} = 3$
- unsatisfiable core:  $\neg x_1 \vee x_3, \neg x_7 \vee x_2, x_7 \vee x_2, x_1 \vee \neg x_2, \neg x_1 \vee \neg x_3$   
 $\chi = \chi \cup \text{CNF}(e_1 = 1)$   
 $\text{cost} = 4$

## Example

$\chi$ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
$\varphi$ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7 \vee d_1$
	$\neg x_7 \vee x_8 \vee d_2$	$\neg x_7 \vee \neg x_8 \vee x_6 \vee d_3$	$\neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4$	$\neg x_1 \vee \neg x_3 \vee e_1$

- ▶ unsatisfiable core:  $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$   
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$   
 $\text{cost} = 1$
- ▶ unsatisfiable core:  $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$   
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$   
 $\text{cost} = 2$
- ▶ unsatisfiable core:  $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$   
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$   
 $\text{cost} = 3$
- ▶ unsatisfiable core:  $\neg x_1 \vee x_3, \neg x_7 \vee x_2, x_7 \vee x_2, x_1 \vee \neg x_2, \neg x_1 \vee \neg x_3$   
 $\chi = \chi \cup \text{CNF}(e_1 = 1)$   
 $\text{cost} = 4$
- ▶ satisfiable

## Example

$\chi$ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
$\varphi$ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7 \vee d_1$
	$\neg x_7 \vee x_8 \vee d_2$	$\neg x_7 \vee \neg x_8 \vee x_6 \vee d_3$	$\neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4$	$\neg x_1 \vee \neg x_3 \vee e_1$

- ▶ unsatisfiable core:  $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$   
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$   
 $\text{cost} = 1$
- ▶ unsatisfiable core:  $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$   
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$   
 $\text{cost} = 2$
- ▶ unsatisfiable core:  $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$   
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$   
 $\text{cost} = 3$
- ▶ unsatisfiable core:  $\neg x_1 \vee x_3, \neg x_7 \vee x_2, x_7 \vee x_2, x_1 \vee \neg x_2, \neg x_1 \vee \neg x_3$   
 $\chi = \chi \cup \text{CNF}(e_1 = 1)$   
 $\text{cost} = 4$
- ▶ satisfiable
- ▶  $\text{pminUNSAT}(\chi, \varphi) = 4$

## Example

$\chi$ :	$\neg x_1 \vee x_3$	$\neg x_7 \vee x_2$	$x_7 \vee x_2$	$x_1 \vee \neg x_2$
$\varphi$ :	$\neg x_1 \vee \neg x_2 \vee b_1$	$\neg x_1 \vee x_2 \vee b_2$	$\neg x_1 \vee x_7$	$x_1 \vee b_3$
	$\neg x_3 \vee x_4 \vee c_1$	$x_3 \vee c_2$	$\neg x_3 \vee \neg x_4 \vee c_3$	$x_4 \vee x_5$
	$\neg x_4 \vee x_5$	$x_1 \vee \neg x_5 \vee x_6$	$x_5 \vee \neg x_6$	$x_7 \vee d_1$
	$\neg x_7 \vee x_8 \vee d_2$	$\neg x_7 \vee \neg x_8 \vee x_6 \vee d_3$	$\neg x_7 \vee \neg x_8 \vee \neg x_6 \vee d_4$	$\neg x_1 \vee \neg x_3 \vee e_1$

- ▶ unsatisfiable core:  $\neg x_1 \vee \neg x_2, \neg x_1 \vee x_2, x_1$   
 $\chi = \chi \cup \text{CNF}(b_1 + b_2 + b_3 = 1)$   
 $\text{cost} = 1$
- ▶ unsatisfiable core:  $\neg x_3 \vee x_4, x_3, \neg x_3 \vee \neg x_4$   
 $\chi = \chi \cup \text{CNF}(c_1 + c_2 + c_3 = 1)$   
 $\text{cost} = 2$
- ▶ unsatisfiable core:  $x_7, \neg x_7 \vee x_8, \neg x_7 \vee \neg x_8 \vee x_6, \neg x_7 \vee \neg x_8 \vee \neg x_6$   
 $\chi = \chi \cup \text{CNF}(d_1 + d_2 + d_3 + d_4 = 1)$   
 $\text{cost} = 3$
- ▶ unsatisfiable core:  $\neg x_1 \vee x_3, \neg x_7 \vee x_2, x_7 \vee x_2, x_1 \vee \neg x_2, \neg x_1 \vee \neg x_3$   
 $\chi = \chi \cup \text{CNF}(e_1 = 1)$   
 $\text{cost} = 4$
- ▶ satisfiable:  $v(x_1) = v(x_2) = v(x_3) = v(x_5) = v(x_7) = \text{T}$  and  $v(x_i) = \text{F}$  otherwise
- ▶  $\text{pminUNSAT}(\chi, \varphi) = 4$  and  $\text{pmaxSAT}(\chi, \varphi) = 12$

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**Algorithm** FuMalik( $\chi, \varphi$ )

---

**Input:** soft clauses  $\varphi$  and satisfiable hard clauses  $\chi$

**Output:** pminUNSAT( $\chi, \varphi$ )

$cost \leftarrow 0$

**while**  $\neg \text{SAT}(\chi \cup \varphi)$  **do**

$UC \leftarrow \text{unsatCore}(\chi \cup \varphi)$

▷  $UC$  must be minimal

$B \leftarrow \emptyset$

**for**  $C \in UC \cap \varphi$  **do**

▷ loop over soft clauses in core

$\varphi \leftarrow \varphi \setminus \{C\} \cup \{C \vee b\}$

▷  $b$  is fresh “blocking” variable

$B \leftarrow B \cup \{b\}$

$\chi \leftarrow \chi \cup \text{CNF}(\sum_{b \in B} b = 1)$

▷ cardinality constraint is hard

$cost \leftarrow cost + 1$

**return**  $cost$

---



---

**Algorithm** FuMalik( $\chi, \varphi$ )

---

**Input:** soft clauses  $\varphi$  and satisfiable hard clauses  $\chi$

**Output:**  $\text{pminUNSAT}(\chi, \varphi)$

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**return**  $cost$

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$\chi \leftarrow \chi \cup \text{CNF}(\sum_{b \in B} b = 1)$

▷ cardinality constraint is hard

$cost \leftarrow cost + 1$

**return**  $cost$

---

---

**Algorithm** FuMalik( $\chi, \varphi$ )

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**Output:** pminUNSAT( $\chi, \varphi$ )

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$\varphi \leftarrow \varphi \setminus \{C\} \cup \{C \vee b\}$

▷  $b$  is fresh “blocking” variable

$B \leftarrow B \cup \{b\}$

$\chi \leftarrow \chi \cup \text{CNF}(\sum_{b \in B} b = 1)$

▷ cardinality constraint is hard

$cost \leftarrow cost + 1$

**return**  $cost$

---

---

**Algorithm** FuMalik( $\chi, \varphi$ )

---

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**Output:** pminUNSAT( $\chi, \varphi$ )

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$\varphi \leftarrow \varphi \setminus \{C\} \cup \{C \vee b\}$

▷  $b$  is fresh “blocking” variable

$B \leftarrow B \cup \{b\}$

$\chi \leftarrow \chi \cup \text{CNF}(\sum_{b \in B} b = 1)$

▷ cardinality constraint is hard

$cost \leftarrow cost + 1$

**return**  $cost$

---

---

**Algorithm** FuMalik( $\chi, \varphi$ )

---

**Input:** soft clauses  $\varphi$  and satisfiable hard clauses  $\chi$

**Output:** pminUNSAT( $\chi, \varphi$ )

$cost \leftarrow 0$

**while**  $\neg \text{SAT}(\chi \cup \varphi)$  **do**

$UC \leftarrow \text{unsatCore}(\chi \cup \varphi)$

▷  $UC$  must be minimal

$B \leftarrow \emptyset$

**for**  $C \in UC \cap \varphi$  **do**

▷ loop over soft clauses in core

$\varphi \leftarrow \varphi \setminus \{C\} \cup \{C \vee b\}$

▷  $b$  is fresh “blocking” variable

$B \leftarrow B \cup \{b\}$

$\chi \leftarrow \chi \cup \text{CNF}(\sum_{b \in B} b = 1)$

▷ cardinality constraint is hard

$cost \leftarrow cost + 1$

**return**  $cost$

---

---

**Algorithm** FuMalik( $\chi, \varphi$ )

---

**Input:** soft clauses  $\varphi$  and satisfiable hard clauses  $\chi$

**Output:** pminUNSAT( $\chi, \varphi$ )

$cost \leftarrow 0$

**while**  $\neg \text{SAT}(\chi \cup \varphi)$  **do**

$UC \leftarrow \text{unsatCore}(\chi \cup \varphi)$

▷  $UC$  must be minimal

$B \leftarrow \emptyset$

**for**  $C \in UC \cap \varphi$  **do**

▷ loop over soft clauses in core

$\varphi \leftarrow \varphi \setminus \{C\} \cup \{C \vee b\}$

▷  $b$  is fresh “blocking” variable

$B \leftarrow B \cup \{b\}$

$\chi \leftarrow \chi \cup \text{CNF}(\sum_{b \in B} b = 1)$

▷ cardinality constraint is hard

$cost \leftarrow cost + 1$

**return**  $cost$

---

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**Algorithm** FuMalik( $\chi, \varphi$ )

---

**Input:** soft clauses  $\varphi$  and satisfiable hard clauses  $\chi$

**Output:** pminUNSAT( $\chi, \varphi$ )

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**Algorithm** FuMalik( $\chi, \varphi$ )

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**Output:**  $\text{pminUNSAT}(\chi, \varphi)$

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**while**  $\neg\text{SAT}(\chi \cup \varphi)$  **do**

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---

## Theorem

$\text{FuMalik}(\chi, \varphi) = \text{pminUNSAT}(\chi, \varphi)$

## Unsatisfiable Cores in z3

```
from z3 import *

x1,x2,x3 = Bool("x1"), Bool("x2"), Bool("x3")
phi = [ Or(Not(x1), Not(x2)), Or(Not(x1), x2),\
        Or(Not(x1), x3), x1, Or(Not(x3), x2)]

solver = Solver()
solver.set(unsat_core=True)

# assert clauses in phi with names phi0 ... phi4
for i,c in enumerate(phi):
    solver.assert_and_track(c, "phi" + str(i))

if solver.check() == z3.unsat:
    uc = solver.unsat_core()
    print(uc) # [phi0, phi1, phi3]
```



Nachum Dershowitz, Ziyad Hanna, and Alexander Nadel.

**A Scalable Algorithm for Minimal Unsatisfiable Core Extraction.**

Proc. 9th Conference on Theory and Applications of Satisfiability, pp. 36–41, 2006.



Yoonna Oh, Maher Mneimneh, Zaher Andraus, Karem Sakallah, and Igor Markov

**AMUSE: A Minimally-Unsatisfiable Subformula Extractor.**

Proc. 41st Design Automation Conference, pp. 518–523, 2004.



Zhaohui Fu and Sharad Malik.

**On solving the partial MAX-SAT problem.**

In Proc. 9th Conference on Theory and Applications of Satisfiability, pp. 252–265, 2006