

SAT and SMT Solving

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Outline

- Summary of Last Week
- Satisfiability Modulo Theories
- DPLL(T)
- Using SMT Solvers with Theories

Definitions

for unsatisfiable CNF formula φ given as set of clauses

- ▶ $\psi \subseteq \varphi$ such that $\bigwedge_{C \in \psi} C$ is unsatisfiable is **unsatisfiable core (UC)** of φ
- ▶ **minimal unsatisfiable core** ψ is UC such that every subset of ψ is satisfiable
- ▶ **SUC** (minimum unsatisfiable core) is UC such that $|\psi|$ is minimal

Remark

SUC is always minimal unsatisfiable core

Definition (Resolution Graph)

directed acyclic graph $G = (V, E)$ is **resolution graph** for set of clauses φ if

1. $V = V_i \uplus V_c$ is set of clauses and $V_i = \varphi$,
2. V_i nodes have no incoming edges,
3. there is exactly one node \square without outgoing edges,
4. $\forall C \in V_c \exists$ edges $D \rightarrow C, D' \rightarrow C$ such that C is resolvent of D and D' , and
5. there are no other edges.

Algorithm $\text{minUnsatCore}(\varphi)$

Input: unsatisfiable formula φ

Output: minimal unsatisfiable core of φ

build resolution graph $G = (V_i \uplus V_c, E)$ for φ

while \exists unmarked clause in V_i **do**

$C \leftarrow$ unmarked clause in V_i

if $\text{SAT}(\overline{\text{Reach}_G(C)})$ **then**

 mark C

\triangleright subgraph without C satisfiable?

$\triangleright C$ is UC member

else

 build resolution graph $G' = (V'_i \uplus V'_c, E')$ for $\overline{\text{Reach}_G(C)}$

$V_i \leftarrow V_i \setminus \{C\}$ and $V_c \leftarrow V'_c \cup (V_c \setminus \text{Reach}_G(C))$

$E \leftarrow E' \cup (E \setminus \text{Reach}_G^E(C))$

$G \leftarrow (V_i \cup V_c, E)$

$G \leftarrow G|_{B\text{Reach}_G(\square)}$

\triangleright restrict to nodes with path to \square

return V_i

Theorem

if φ unsatisfiable then $\text{minUnsatCore}(\varphi)$ is minimal unsatisfiable core of φ

Definition (Partial minUNSAT)

$\text{pminUNSAT}(\chi, \varphi)$ is minimal $|\psi|$ such that $\psi \subseteq \varphi$ and $\chi \wedge \bigwedge_{C \in \psi} \neg C$ satisfiable

Algorithm FuMalik(χ, φ)

Input: clause set φ and satisfiable clause set χ

$cost \leftarrow 0$

while $\neg \text{SAT}(\chi \cup \varphi)$ **do**

$UC \leftarrow \text{unsatCore}(\chi \cup \varphi)$

▷ must be minimal

$B \leftarrow \emptyset$

for $C \in UC \cap \varphi$ **do**

▷ loop over soft clauses in core

$b \leftarrow$ new blocking variable

$\varphi \leftarrow \varphi \setminus \{C\} \cup \{C \vee b\}$

$B \leftarrow B \cup \{b\}$

$\chi \leftarrow \chi \cup \text{CNF}(\sum_{b \in B} b = 1)$

▷ cardinality constraint is hard

$cost \leftarrow cost + 1$

return $cost$

Theorem

$\text{FuMalik}(\chi, \varphi) = \text{pminUNSAT}(\chi, \varphi)$

$|\varphi| = \text{pminUNSAT}(\chi, \varphi) + \text{pmaxSAT}(\chi, \varphi)$

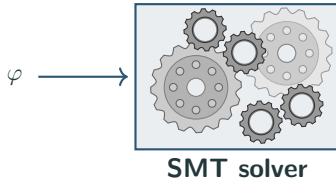


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- Summary of Last Week
- Satisfiability Modulo Theories
 - Recap: First-Order Logic
 - Eager and Lazy Paradigms
- DPLL(T)
- Using SMT Solvers with Theories

SMT Solving

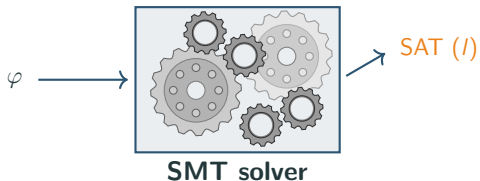
input: formula φ involving theory T
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SMT Solving

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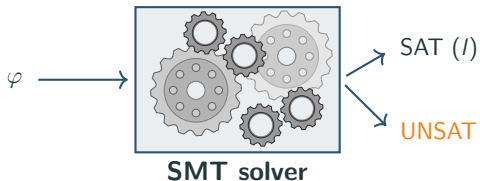
output: SAT + valuation v such that $v(\varphi) = T$ if φ is T -satisfiable



SMT Solving

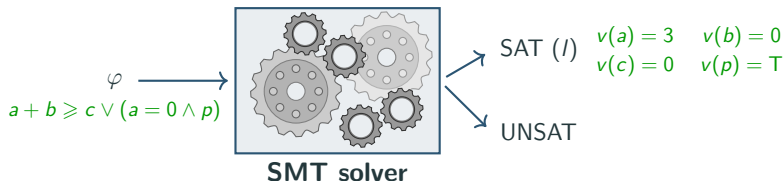
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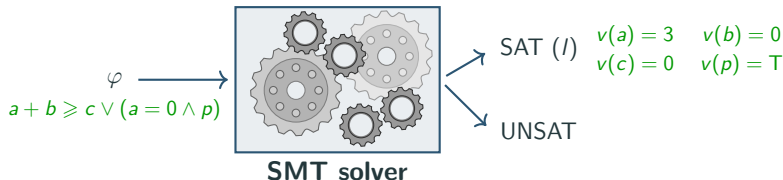
Example (Common theories)

► arithmetic

$$2a + b \geq c \vee (a - b = c + 3 \wedge p)$$

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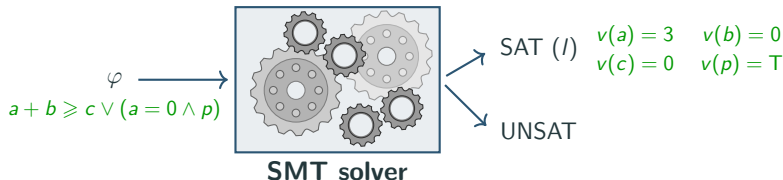
Example (Common theories)

- ▶ arithmetic
- ▶ uninterpreted functions

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$$f(x, y) \neq f(y, x) \wedge g(a) = a \rightarrow g(f(x, x)) = g(y)$$

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Example (Common theories)

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- ▶ uninterpreted functions
- ▶ bit vectors

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$$f(x, y) \neq f(y, x) \wedge g(a) = a \rightarrow g(f(x, x)) = g(y)$$
$$((\text{zext}_{32} \ a_8) + b_{32}) \times c_{32} >_u 0_{32}$$

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First-Order Logic: Syntax

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- ▶ **Σ -terms** t are built according to grammar

$$t ::= x \mid c \mid f(\underbrace{t, \dots, t}_n)$$

for constant $c \in \mathcal{F}$, function symbol $f \in \mathcal{F}$ of arity $n > 0$, and variable $x \in X$

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$$\varphi ::= Q \mid P(\underbrace{t, \dots, t}_n) \mid \perp \mid \top \mid \neg\varphi \mid \varphi \wedge \varphi \mid \varphi \vee \varphi \mid \forall x. \varphi \mid \exists x. \varphi$$

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- ▶ variable x is **free** in φ if it is not bound by quantifier above

Notation

write f/n or P/n to express that f or P have arity n

Example

- ▶ let $\Sigma = \langle \mathcal{F}, \mathcal{P} \rangle$ with $\mathcal{F} := \{a/0, b/0, f/1, g/2\}$

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 - ▶ x , y , and z
 - ▶ $g(a, f(x))$ and $g(g(a, y), f(b))$

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- ▶ the following are Σ -formulas (free variables **highlighted**):
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Definition (Model)

model \mathcal{M} for signature $\Sigma = \langle \mathcal{F}, \mathcal{P} \rangle$ consists of

- 1 non-empty set A (universe of concrete values)
- 2 function $f^{\mathcal{M}}: A^n \rightarrow A$ for every n -ary $f \in \mathcal{F}$
- 3 set of n -tuples $P^{\mathcal{M}} \subseteq A^n$ for every n -ary $P \in \mathcal{P}$

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function and predicate symbols $\mathcal{F} = \{f/1, a/0\}$ and $\mathcal{P} = \{R/2\}$

- 1 model \mathcal{M}_1 : universe $A_1 = \mathbb{N}$
 $f^{\mathcal{M}_1}(x) = 2x + 1$
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- 2 model \mathcal{M}_2 : universe A_2 is set of all Twitter users
 $f^{\mathcal{M}_2}(x) = \text{last person who started following } x \text{ (or } x \text{ if no follower)}$
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Definitions

- ▶ **environment** for model $\mathcal{M} = \langle A, \{f^{\mathcal{M}}\}_{f \in \mathcal{F}}, \{P^{\mathcal{M}}\}_{P \in \mathcal{P}} \rangle$ is mapping $I: X \rightarrow A$

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- ▶ environment for model $\mathcal{M} = \langle A, \{f^{\mathcal{M}}\}_{f \in \mathcal{F}}, \{P^{\mathcal{M}}\}_{P \in \mathcal{P}} \rangle$ is mapping $I: X \rightarrow A$
- ▶ value $t^{\mathcal{M}, I}$ of term t in model \mathcal{M} wrt environment I is defined inductively:

$$t^{\mathcal{M}, I} = \begin{cases} I(t) & \text{if } t \text{ is a variable} \\ f^{\mathcal{M}}(t_1^{\mathcal{M}, I}, \dots, t_n^{\mathcal{M}, I}) & \text{if } t = f(t_1, \dots, t_n) \end{cases}$$

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- ▶ for environment I , variable x and $a \in A$, **extended environment** $I[x \mapsto a]$ is

$$(I[x \mapsto a])(y) = \begin{cases} a & \text{if } x = y \\ I(y) & \text{otherwise} \end{cases}$$

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- ▶ satisfaction relation $\mathcal{M} \models_I \varphi$ is defined inductively:

$$\mathcal{M} \models_I \varphi \iff \begin{cases} (t_1^{\mathcal{M}, I}, \dots, t_n^{\mathcal{M}, I}) \in P^{\mathcal{M}} & \text{if } \varphi = P(t_1, \dots, t_n) \\ \mathcal{M} \not\models_I \psi & \text{if } \varphi = \neg \psi \\ \mathcal{M} \models_I \varphi_1 \text{ and } \mathcal{M} \models_I \varphi_2 & \text{if } \varphi = \varphi_1 \wedge \varphi_2 \\ \mathcal{M} \models_I \varphi_1 \text{ or } \mathcal{M} \models_I \varphi_2 & \text{if } \varphi = \varphi_1 \vee \varphi_2 \\ \mathcal{M} \models_{I[x \mapsto a]} \psi \text{ for all } a \in A & \text{if } \varphi = \forall x. \psi \\ \mathcal{M} \models_{I[x \mapsto a]} \psi \text{ for some } a \in A & \text{if } \varphi = \exists x. \psi \end{cases}$$

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EQ-satisfiable
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Definition (Theory of Equality EQ)

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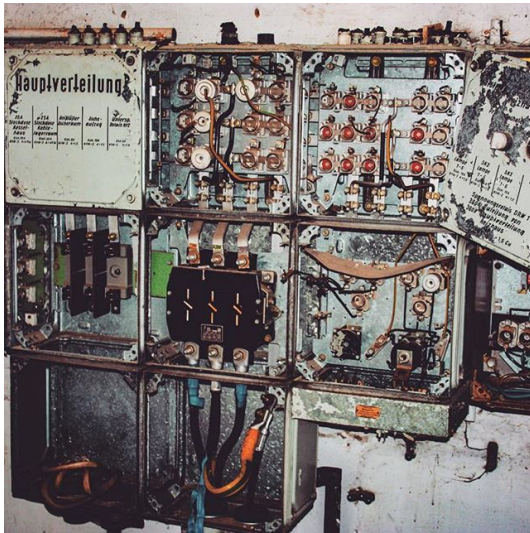
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Uninterpreted Functions in Real Life



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- ▶ solvers: Yices, OpenSMT, MathSAT, Z3, CVC4, Barcelogic, ...

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Aim

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- ▶ **advantage:** use SAT solver off the shelf

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Aim

given Σ -theory T and Σ -formula φ mixing propositional logic with symbols from Σ , determine T -satisfiability

Approach 1: Eager SMT Solving

- ▶ use satisfiability-preserving transformation from T literals to SAT formula, ship one big formula to SAT solver
- ▶ requires sophisticated translation for each theory:
done for EUF, difference logic, linear integer arithmetic, arrays
- ▶ still dominant approach for bit-vector arithmetic (known as “bit blasting”)
- ▶ **advantage:** use SAT solver off the shelf
- ▶ **drawbacks:**
 - ▶ expensive translations: infeasible for large formulas
 - ▶ even more complicated with multiple theories

The Lazy Paradigm

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Approach 2: Lazy SMT Solving

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 - ▶ “forget theory” by replacing T -literals with fresh propositional variables

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 - ▶ otherwise T -solver generates T -consequence C of φ excluding v , repeat from 1 with $\varphi \wedge C$

Example

$$g(a) = c \wedge (\neg(f(g(a)) = f(c)) \vee g(a) = d) \wedge \neg(c = d)$$

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2 **unsatisfiable**

- Summary of Last Week
- Satisfiability Modulo Theories
- DPLL(T)
- Using SMT Solvers with Theories

Approach

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- ▶ *T-backjump*
$$M \text{ } I^d \text{ } N \parallel F, C \implies M \text{ } I' \parallel F, C$$
if $M \text{ } I^d \text{ } N \models \neg C$ and \exists clause $C' \vee I'$ such that
 - ▶ $F, C \models_T C' \vee I'$
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- ▶ *T-learn*
$$M \parallel F \implies M \parallel F, C$$

if $F \models_T C$ and all atoms of C occur in M or F

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if $F \models_T C$
- ▶ *T-propagate*
$$M \parallel F \implies M I \parallel F$$

if $M \models_T I$, literal I or I^c occurs in F , and I is undefined in M

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- ▶ T -solver checks T -satisfiability of model M whenever literal is added to M

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Improvement 2: On-Line SAT solver

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Improvement 3: Eager Theory Propagation

- ▶ apply T -propagate before decide

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- ▶ T -solver checks T -satisfiability of model M whenever literal is added to M

Improvement 2: On-Line SAT solver

- ▶ after T -learn added clause, apply fail or T -backjump instead of restart

Improvement 3: Eager Theory Propagation

- ▶ apply T -propagate before decide

Remark

all three improvements can be combined

Example (Revisited with DPLL(\mathcal{T}))

$$\underbrace{g(a) = c}_1 \wedge (\underbrace{\neg(f(g(a)) = f(c))}_2 \vee \underbrace{g(a) = d}_3) \wedge \underbrace{\neg(c = d)}_4$$

$$\parallel 1, (\bar{2} \vee 3), \bar{4}$$

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unit propagate

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$$\Rightarrow 1 \parallel 1, (\bar{2} \vee 3), \bar{4} \quad \text{unit propagate}$$

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	$\parallel 1, (\bar{2} \vee 3), \bar{4}$	
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\Rightarrow	$1 \bar{4} 2 3 \parallel 1, (\bar{2} \vee 3), \bar{4}, (\bar{1} \vee 2)$	unit propagate
\Rightarrow	$1 \bar{4} 2 3 \parallel 1, (\bar{2} \vee 3), \bar{4}, (\bar{1} \vee 2), (\bar{1} \vee \bar{3} \vee 4)$	T -learn
\Rightarrow	FailState	fail

Lazyness in $DPLL(T)$



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Lazyness in $DPLL(T)$



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T -solver

Lazyness in $DPLL(T)$



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T -solver

SAT solver

- Summary of Last Week
- Satisfiability Modulo Theories
- DPLL(T)
- Using SMT Solvers with Theories

Example (SMT-LIB 2 for Propositional Logic)

formula $(x_1 \vee \neg x_3) \wedge (x_2 \vee x_3 \vee \neg x_1) \wedge (\neg x_1 \vee x_2 \vee x_3)$ can be expressed by

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(declare-const x1 Bool)
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- ▶ `get-model` prints model (after satisfiability check)

Example (SMT-LIB 2 for EUF)

$f(f(a)) = a \wedge f(a) = b \wedge \neg(a = b)$ is expressed as

```
(declare-sort A)
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EUF in SMT-LIB 2

- terms must have **sort**, so declare fresh sort and use for all symbols:
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- ▶ $(\text{distinct } x y)$ is equivalent to $\text{not}(= x y)$

Example (SMT-LIB 2 for LIA)

$2x \geq y + z \wedge \neg(x = y)$ is expressed as

```
(declare-const x Int)
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Integer Arithmetic in SMT-LIB 2

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- ▶ `=` also covers equality on \mathbb{Z}
- ▶ `<`, `<=`, `>`, `>=` are $<\mathbb{Z}$, $\leq\mathbb{Z}$, $>\mathbb{Z}$, $\geq\mathbb{Z}$

EUf in python/z3

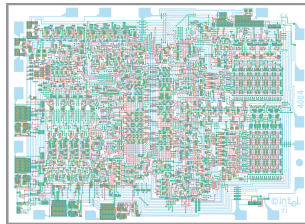
```
A = DeclareSort('A') # new uninterpreted sort named 'A'
a = Const('a', A) # create constant of sort A
b = Const('b', A) # create another constant of sort A
f = Function('f', A, A) # create function of sort A -> A

s = Solver()
s.add(f(f(a)) == a, f(a) == b, a != b)

print(s.check()) # sat
m = s.model()
print("interpretation assigned to A:")
print(m[A]) # [A!val!0, A!val!1]
print("interpretations:")
print(m[f]) # [A!val!0 -> A!val!1, A!val!1 -> A!val!0, ...]
print(m[a]) # A!val!0
print(m[b]) # A!val!1
```

EUF Application: Verification of Microprocessors

- verify that 3-stage pipelined MIPS processor satisfies intended instruction set architecture



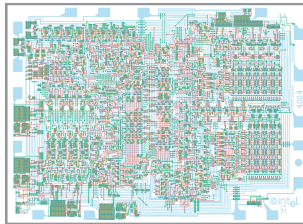
Miroslav N. Velev and Randal E. Bryant.

Bit-level abstraction in the verification of pipelined microprocessors by correspondence checking.

In Proc. of Formal Methods in Computer-Aided Design, pp. 18–35, 1998.

EUF Application: Verification of Microprocessors

- ▶ verify that 3-stage pipelined MIPS processor satisfies intended instruction set architecture
- ▶ encoding
 - ▶ data as bit sequence
 - ▶ memory as uninterpreted function (UF)
 - ▶ computation logic as UF
 - ▶ control logic as uninterpreted predicate



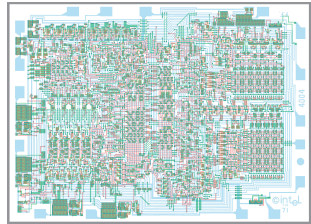
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 - ▶ data as bit sequence
 - ▶ memory as uninterpreted function (UF)
 - ▶ computation logic as UF
 - ▶ control logic as uninterpreted predicate
- ▶ EUF ensures functional consistency:
same data results in same computation



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DPLL(T)



Robert Nieuwenhuis, Albert Oliveras, and Cesare Tinelli.

Solving SAT and SAT Modulo Theories: From an Abstract Davis-Putnam-Logemann-Loveland Procedure to DPLL(T).

Journal of the ACM 53(6), pp. 937–977, 2006.

Application



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