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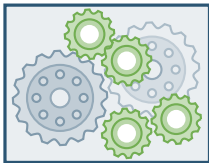
# Runtime Complexity Analysis of Logically Constrained Rewriting

Sarah Winkler and Georg Moser  
University of Verona and University of Innsbruck

LOPSTR 2020  
7 September 2020

# Motivation: Runtime Analysis

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mergesort = function
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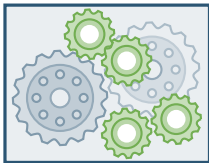


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- ▶ support for full recursion, common data structures and types

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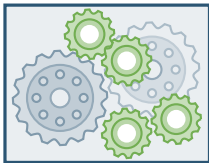
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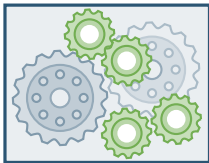
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- ▶ Logically Constrained Rewrite Systems (LCTRS):

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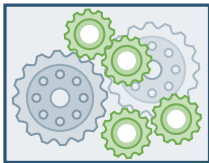
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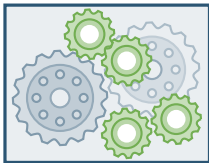
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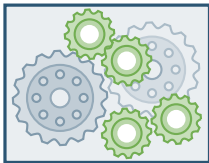
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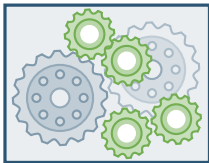
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- ▶ implementation in complexity tool **TcT**

## Example 1: Integer Transition Systems

$\text{init}(x, y, z) \rightarrow \text{sort}(x, y, z)$

$\text{sort}(x, y, z) \rightarrow \langle \text{ms}_0(x, u, v), \text{ms}_1(x, u, v), \text{ms}_2(x, u, v), \text{ms}_3(x, u, v) \rangle$

$[x \geq 2 \wedge u \geq 0 \wedge v \geq 0 \wedge x + 1 \geq 2u \wedge 2u \geq x \wedge x \geq 2v \wedge 2v + 1 \geq x]$

$\text{ms}_0(x, y, z) \rightarrow \text{split}(x, y, z) \quad \text{split}(x, y, z) \rightarrow \text{split}(x - 2, y, z) [x \geq 2]$

$\text{ms}_1(x, y, z) \rightarrow \text{ms}(y, y, z) \quad \text{merge}(x, y, z) \rightarrow \text{merge}(x - 1, y, z) [x \geq 1 \wedge y \geq 1]$

$\text{ms}_2(x, y, z) \rightarrow \text{ms}(z, y, z) \quad \text{merge}(x, y, z) \rightarrow \text{merge}(x, y - 1, z) [x \geq 1 \wedge y \geq 1]$

$\text{ms}_3(x, y, z) \rightarrow \text{merge}(y, z, z)$

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- ▶ LCTRSs cover Integer Transition Systems (ITS)
- ▶ new  $\text{T}_{\text{CT}}$  version derives optimal  $\mathcal{O}(n \log(n))$  bound (CoFloCo, KoAT, PUBS, and previous version of  $\text{T}_{\text{CT}}$  at best quadratic)

## Example 2: Logic Programs

$\text{max\_length}(ls, m, l) \rightarrow \langle \text{max}(ls, 0, m), \text{len}(ls, l) \rangle$

$\text{len}(xs, l) \rightarrow \text{len}(t, l - 1) [xs \approx h :: t]$

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- ▶ new version of **TCT** can handle LCTRSs corresponding to deterministic Prolog programs over integers and lists

## Example 2: Logic Programs

$$\begin{aligned} \max\_length(ls, m, l) &\rightarrow \langle \max(ls, 0, m), \text{len}(ls, l) \rangle \\ \text{len}(xs, l) &\rightarrow \text{len}(t, l - 1) [xs \approx h :: t] & \text{len}([], 0) &\rightarrow \langle \rangle \\ \max(xs, n, m) &\rightarrow \max(t, n, m) [h \leq n \wedge xs \approx h :: t] & \max([], m, m) &\rightarrow \langle \rangle \\ \max(xs, n, m) &\rightarrow \max(t, h, m) [h > n \wedge xs \approx h :: t] \end{aligned}$$

- ▶ can use approach to analyze (constraint) logic programs
- ▶ new version of TCT can handle LCTRSs corresponding to deterministic Prolog programs over integers and lists
- ▶ techniques known to extend to decomposable **non-deterministic** programs

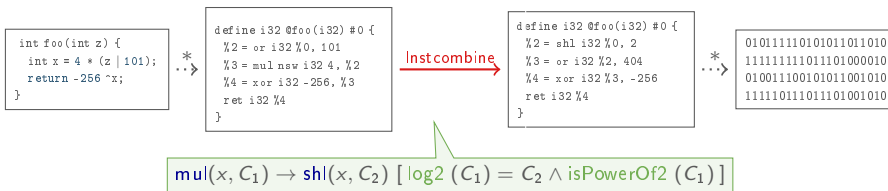


J. Giesl, T. Ströder, P. Schneider-Kamp, F. Emmes, C. Fuhs.

**Symbolic evaluation graphs and term rewriting:  
a general methodology for analyzing logic programs.**

Proc. PPDP 2012, pp. 1–12, 2012.

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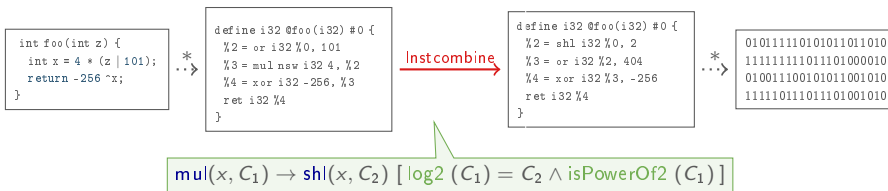


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- ▶ e.g. in LLVM: multiplications to shifts, reordering bitwise operations, ...



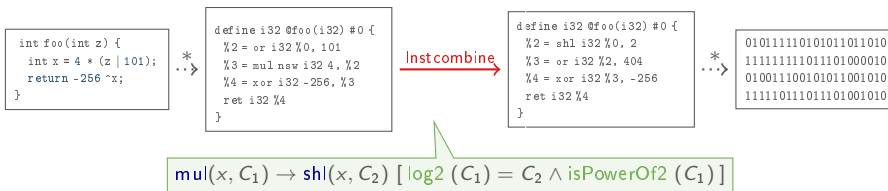
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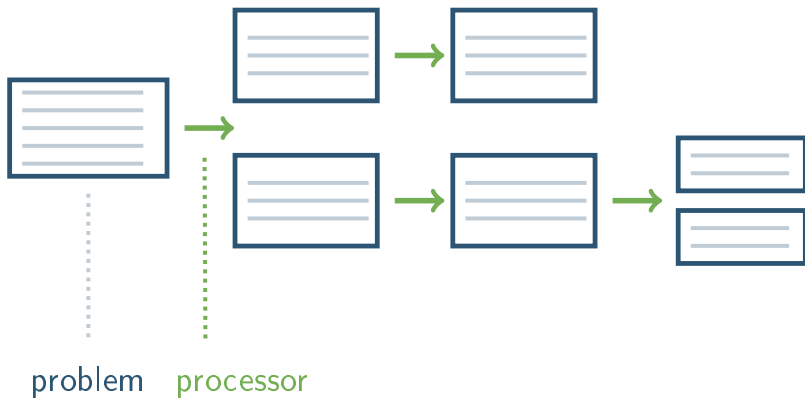
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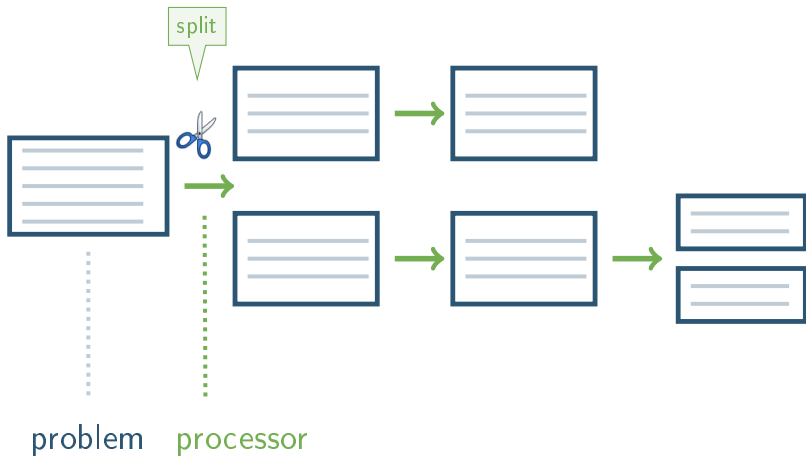
## Expression simplifications in compilers

- ▶ e.g. in LLVM: multiplications to shifts, reordering bitwise operations, ...
- ▶ can be modeled as LCTRS
- ▶ **complexity crucial**  
(current work is first step: derivational complexity needed)

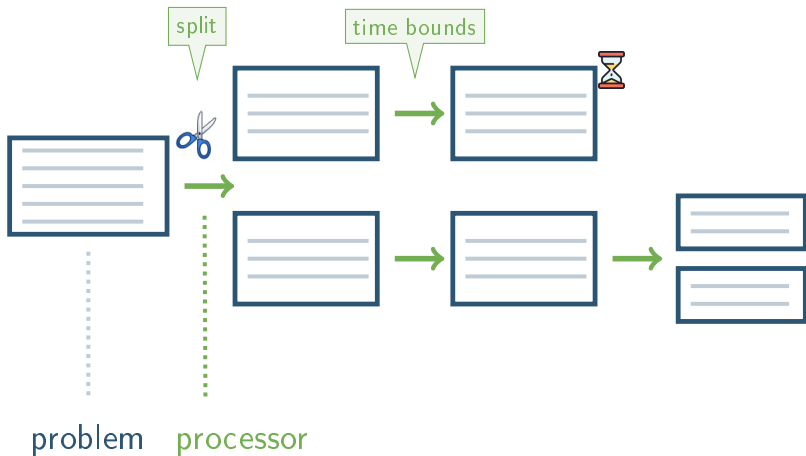
# How? Divide and Conquer



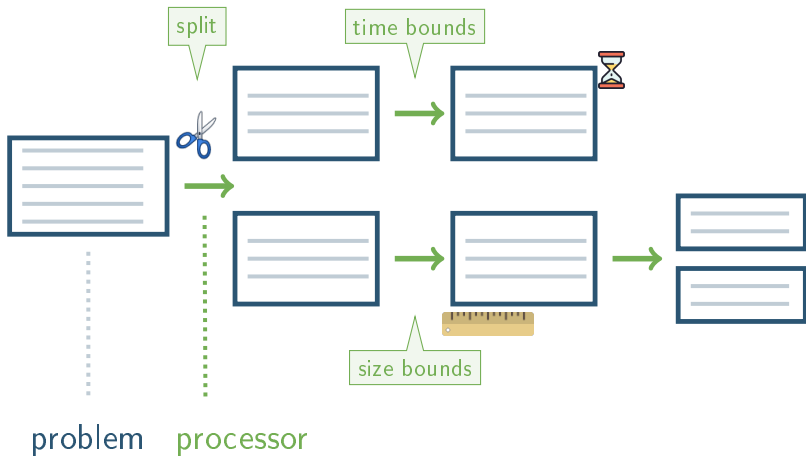
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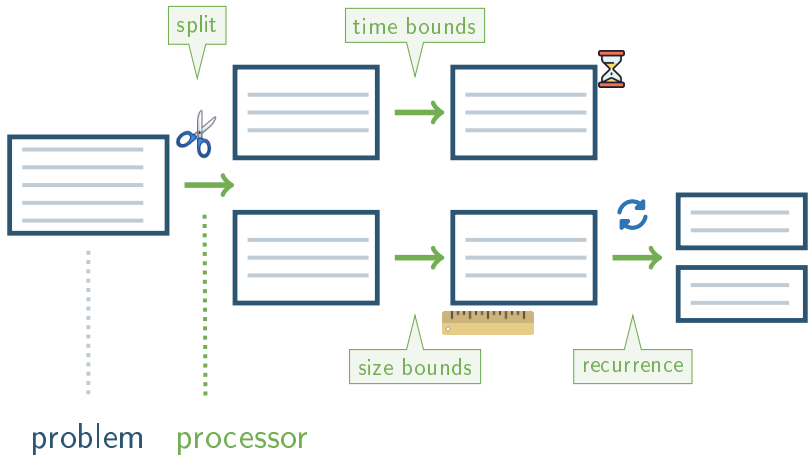
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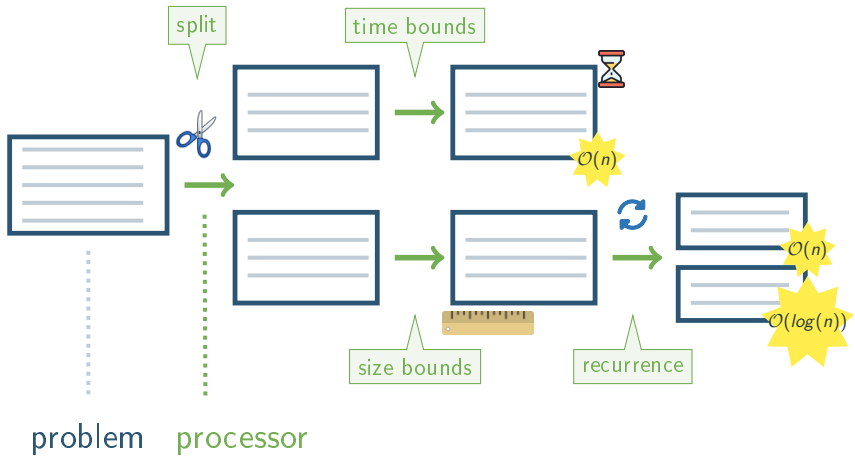
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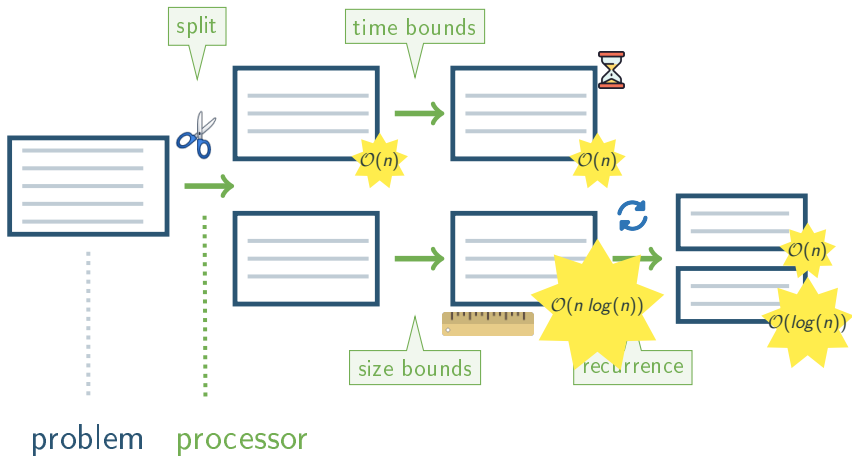


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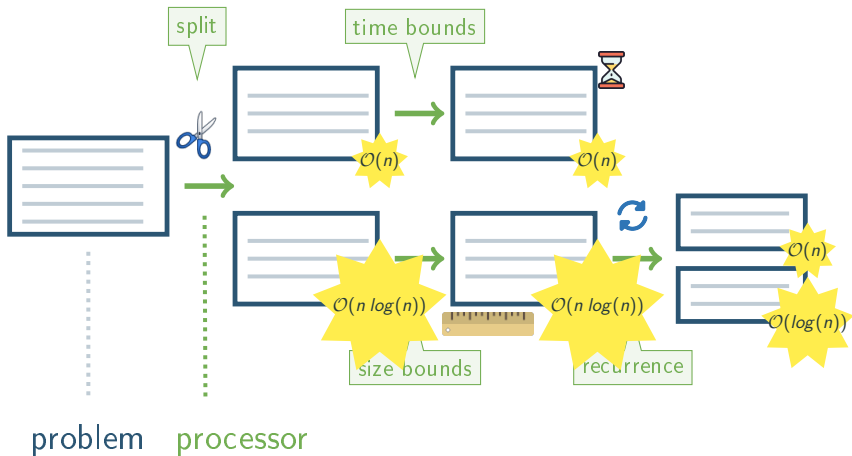




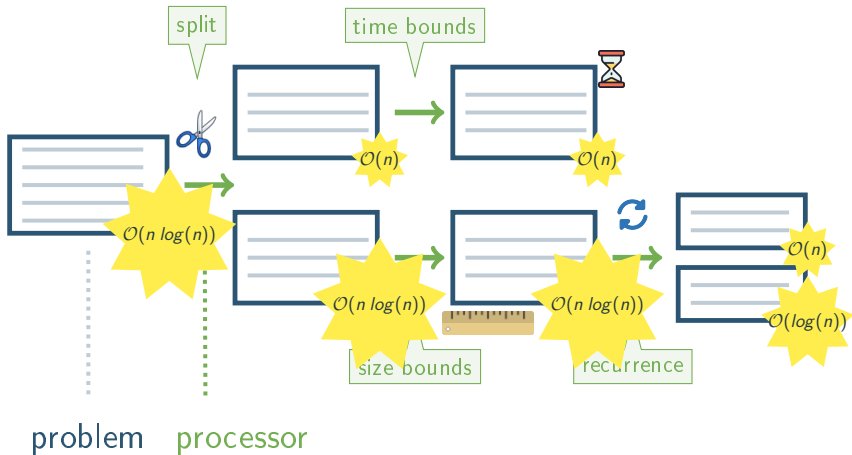
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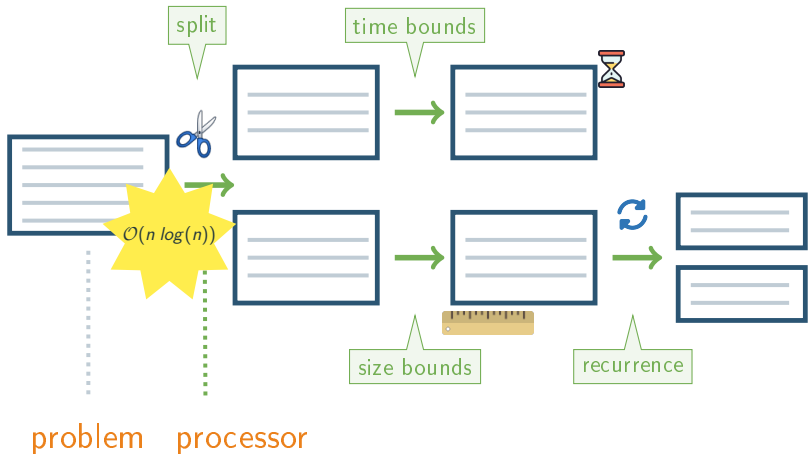
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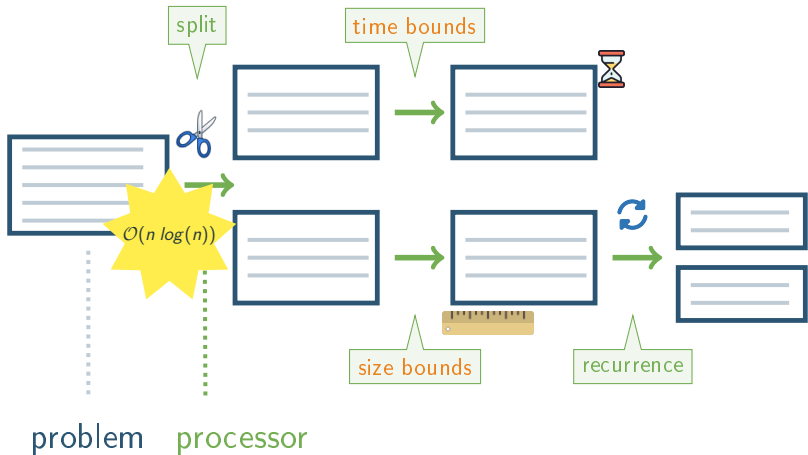



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M. Avanzini and G. Moser.  
**A combination framework for complexity.**  
Inf. Comput., 248:22–55, 2016.

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-  M. Brockschmidt, F. Emmes, S. Falke, C. Fuhs, and J. Giesl.  
**Analyzing runtime and size complexity of integer programs.**  
ACM Trans. Program. Lang. Syst., 38(4):13:1–13:50, 2016.

# Contents

Motivation

Background

Processor Framework

Implementation

Conclusion

## Logically Constrained Rewrite Systems

- ▶ logically constrained rewrite rule

$$l \rightarrow r [c]$$

- ▶ constraint  $c$  is term over logic signature (with SMT-decidable theory)
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- ▶  $\text{mul}(\text{sub}(y, x), c) \rightarrow \text{mul}(\text{sub}(x, y), \text{abs}(c)) [c < \mathbf{0}_8 \wedge \text{isPowerOf2}(\text{abs}(c))]$  (bitvectors)

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$$\text{init}^\#(x) \rightarrow \text{f}^\#(x)$$

$$\text{f}^\#(y) \rightarrow \langle \text{f}^\#(y-1), \text{g}^\#(y) \rangle [y \geq 0]$$

$$\text{g}^\#(z) \rightarrow \text{g}^\#(z/2) [z \geq 0]$$

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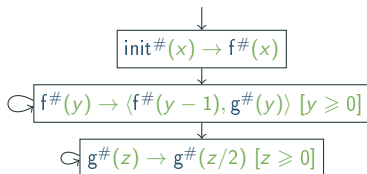
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## Bound expressions

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measure  $|x| \in \mathbb{N}$  for all  
input variables  $x$

## Bound expressions

$$\text{UB} ::= |x| \mid \text{UB} + \text{UB} \mid \text{UB} \cdot \text{UB} \mid \max(\text{UB}, \text{UB}) \mid \text{UB}^k \mid \log_k(\text{UB}) \mid \omega$$

## Time bounds and size bounds

for LCTRS  $\mathcal{R}$ , let  $\rho: \ell \rightarrow r [c] \in \text{DT}(\mathcal{R})$  and consider rewrite sequence:

$$\text{init}^\#(x_1, \dots, x_n) \rightarrow \dots \xrightarrow[\rho]{\sigma_1} \rightarrow \dots \xrightarrow[\rho]{\sigma_2} \rightarrow \dots \xrightarrow[\rho]{\sigma_m} \rightarrow \dots \quad (\star)$$

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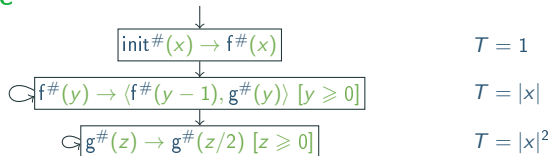
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## Example



## Bound expressions

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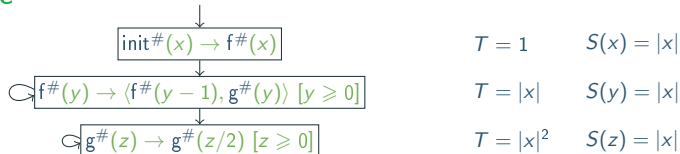
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## Example



## Definitions

given LCTRS  $\mathcal{R}$ ,

- ▶ **complexity problem** is  $P = (t_0, \mathcal{D}, \mathcal{R})$  for initial term  $t_0$  and DTs  $\mathcal{D}$

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# Processor Framework

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- ▶ judgement  $\vdash P : (T, S)$  states time bounds  $T$  and size bounds  $S$  for  $P$
- ▶ **processor** Proc is inference rule on complexity judgements

$$\frac{\vdash P_1 : (T_1, S_1), \dots, \vdash P_k : (T_k, S_k)}{\vdash P : (T, S)} \text{ Proc}$$

# Processor Framework

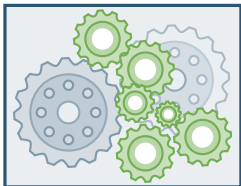
## Definitions

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## Processors



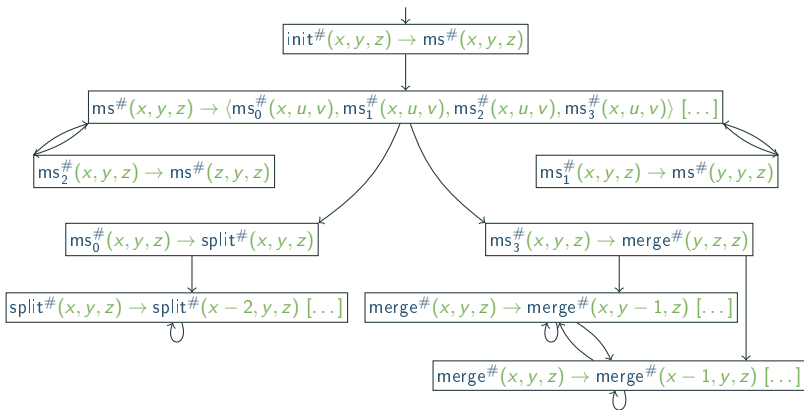
- ▶ interpretations
- ▶ time bounds
- ▶ size bounds
- ▶ **splitting**
- ▶ **recurrence**
- ▶ chaining, simplification

new

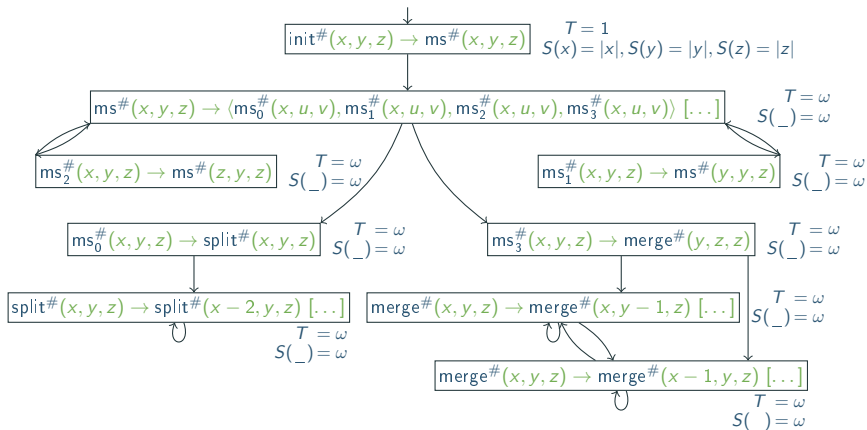
new



## Example (Bounding mergesort)

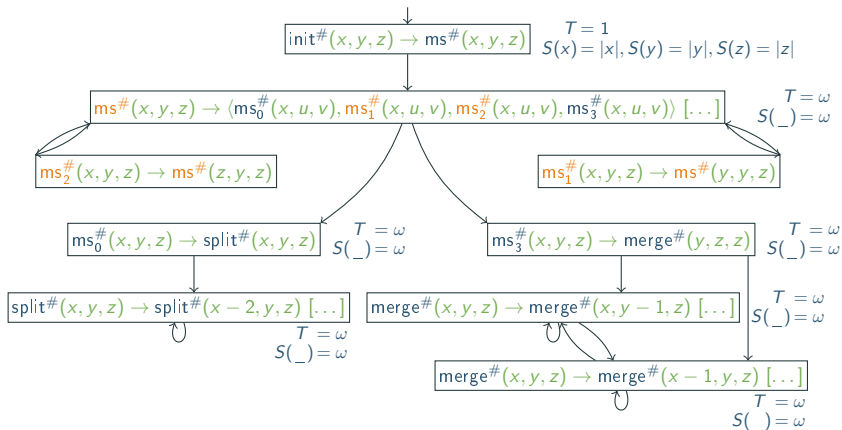


## Example (Bounding mergesort)



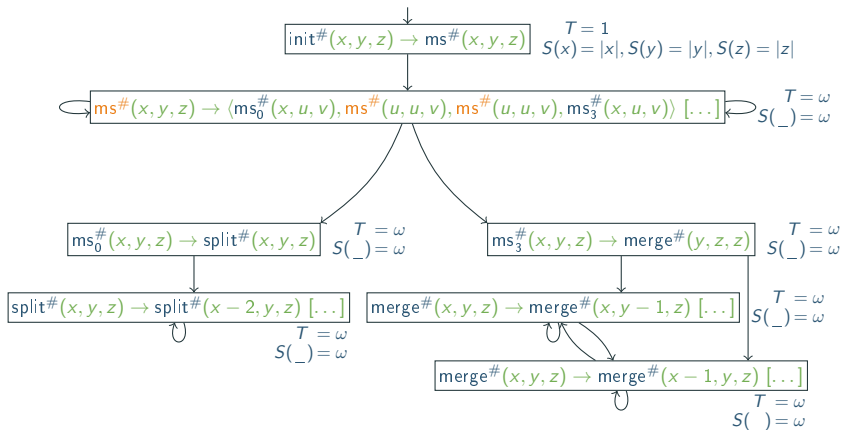
initial problem

## Example (Bounding mergesort)



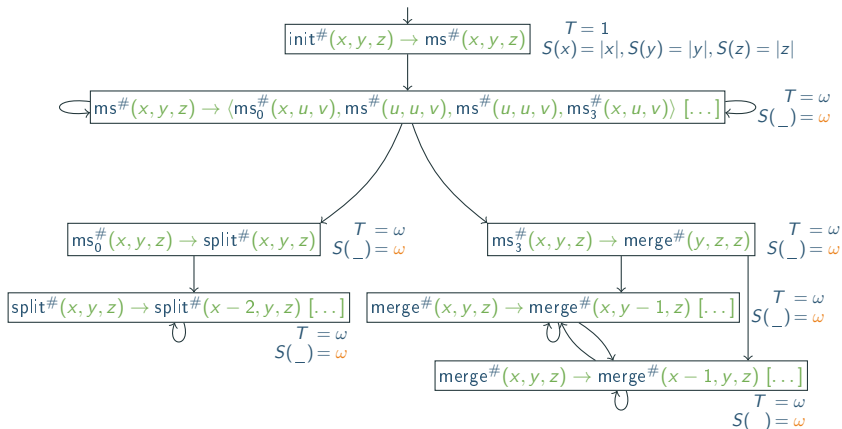
### 1 Chaining processor

## Example (Bounding mergesort)



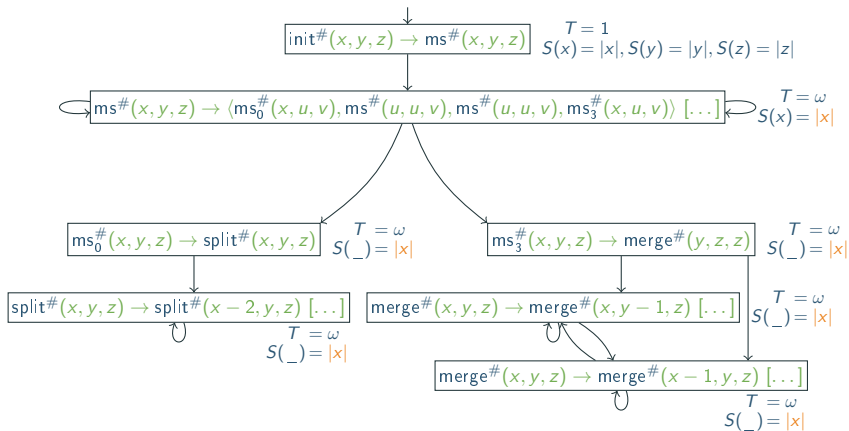
### 1 Chaining processor

## Example (Bounding mergesort)



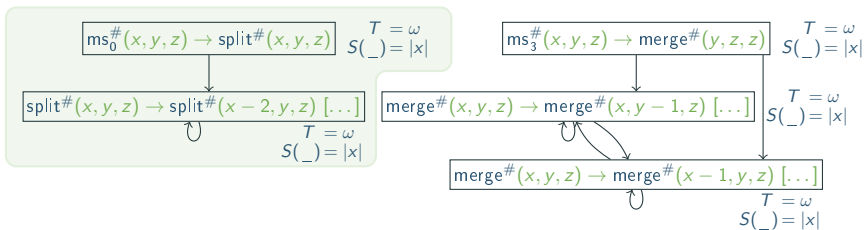
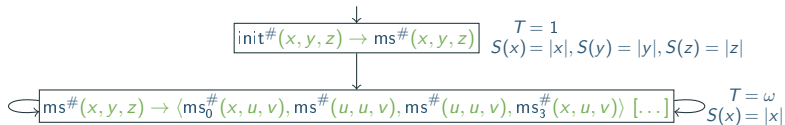
- 1 Chaining processor
- 2 Size bounds processor

## Example (Bounding mergesort)



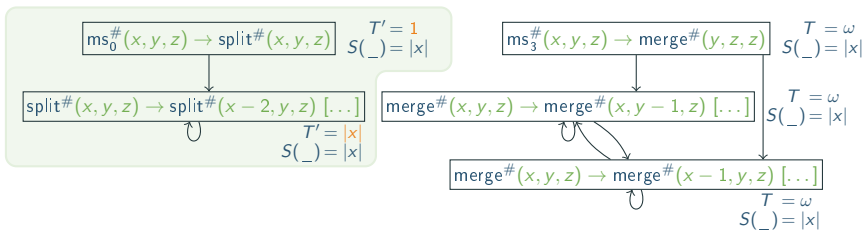
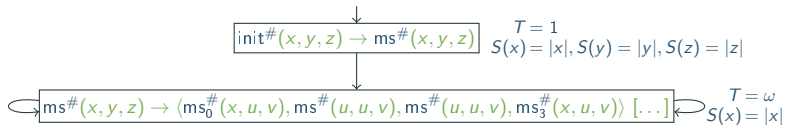
- 1 Chaining processor
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## Example (Bounding mergesort)



- 1 Chaining processor
- 2 Size bounds processor
- 3 Split processor

## Example (Bounding mergesort)

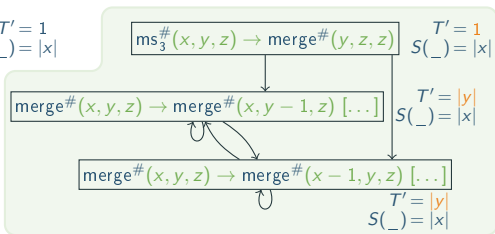
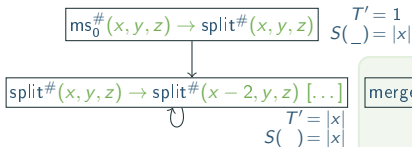
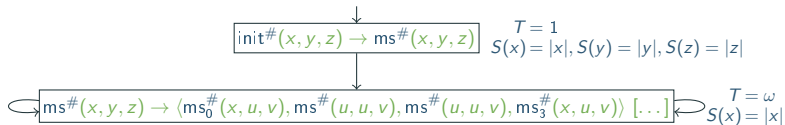


- 1 Chaining processor
- 2 Size bounds processor
- 3 Split processor

- 4 Interpretation processor



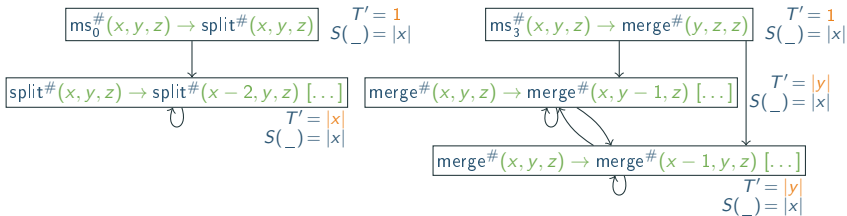
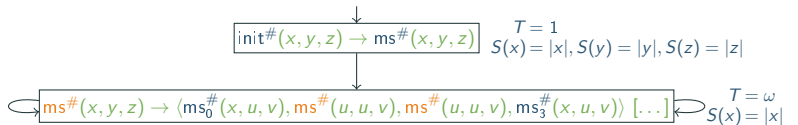
## Example (Bounding mergesort)



- 1 Chaining processor
- 2 Size bounds processor
- 3 Split processor

- 4 Interpretation processor
- 5 Time bounds processor<sup>+</sup>

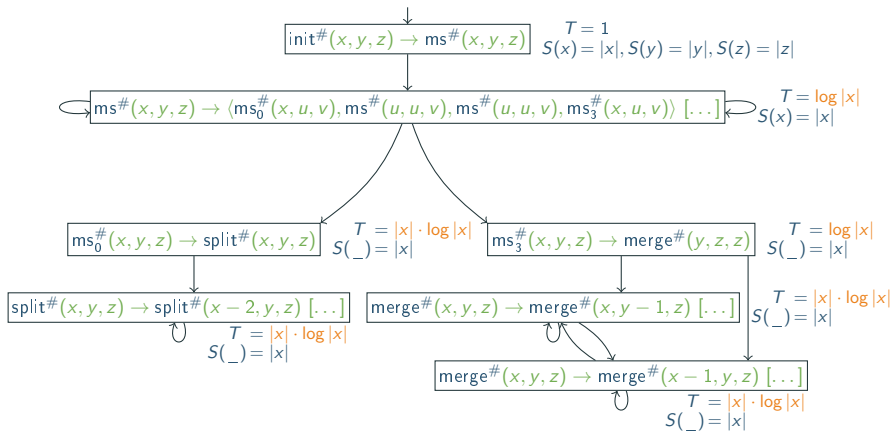
## Example (Bounding mergesort)



- 1 Chaining processor
- 2 Size bounds processor
- 3 Split processor

- 4 Interpretation processor
- 5 Time bounds processor<sup>+</sup>
- 6 Recurrence processor

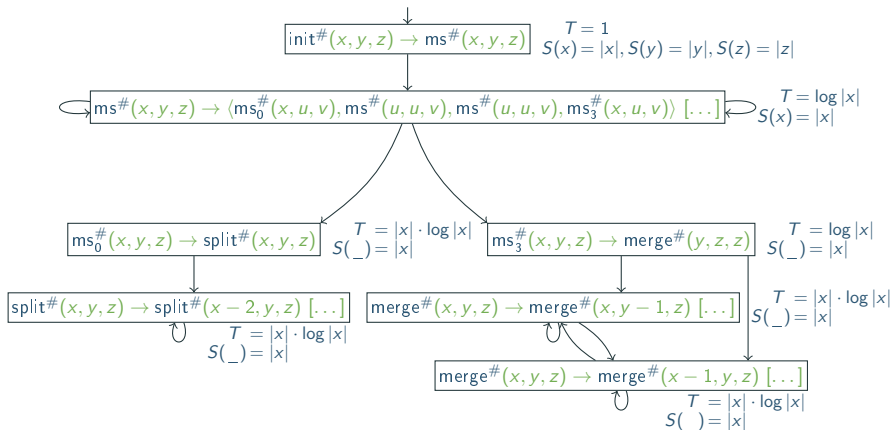
## Example (Bounding mergesort)



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## Example (Bounding mergesort)



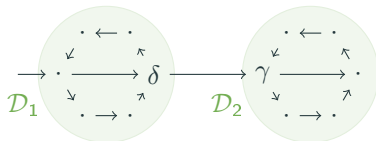
- 1 Chaining processor
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$$\sum T \in \mathcal{O}(|x| \cdot \log |x|)$$

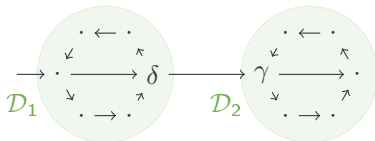
## Splitting Processor

Let  $P = (t_0, \mathcal{D}, \mathcal{R})$  have dependency graph of shape



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Then the following processor is sound

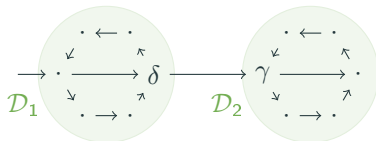
$\vdash P: (T, S)$

---

Split

## Splitting Processor

Let  $P = (t_0, \mathcal{D}, \mathcal{R})$  have dependency graph of shape



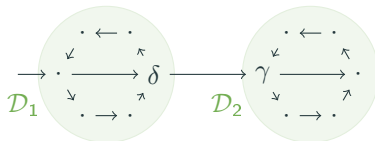
Then the following processor is sound

$$\frac{\vdash P : (T, S) \quad \vdash (t_0, \mathcal{D}_1, \mathcal{R}) : (T_1, S_1)}{\text{Split}}$$

Split

## Splitting Processor

Let  $P = (t_0, \mathcal{D}, \mathcal{R})$  have dependency graph of shape



Then the following processor is sound, where  $\gamma: \ell \rightarrow r [\psi]$

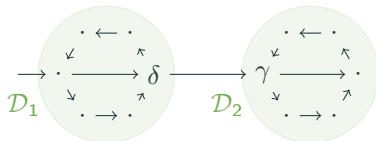
$$\frac{\vdash P: (T, S) \quad \vdash (t_0, \mathcal{D}_1, \mathcal{R}): (T_1, S_1) \quad \vdash (\ell, \mathcal{D}_2, \mathcal{R}): (T_2, S_2)}{\text{Split}}$$

Split



## Splitting Processor

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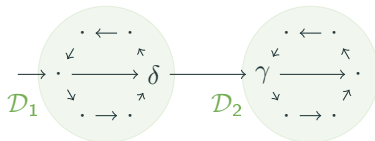


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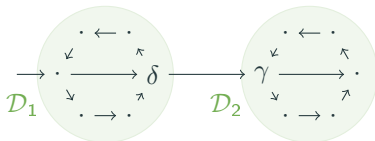


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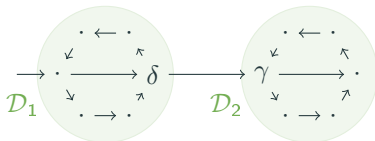


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## Splitting Processor

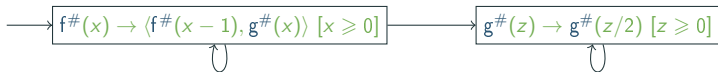
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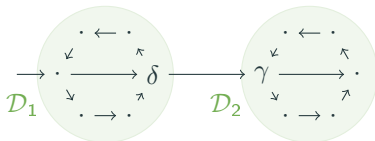
$$\frac{\vdash P: (T, S) \quad \vdash (t_0, \mathcal{D}_1, \mathcal{R}): (T_1, S_1) \quad \vdash (\ell, \mathcal{D}_2, \mathcal{R}): (T_2, S_2)}{\vdash P: (\lambda \rho. \left\{ \begin{array}{ll} T_1(\rho) & \text{if } \rho \in \mathcal{D}_1 \\ T(\delta) \cdot T_2(\rho)(S(\gamma, y_1), \dots, S(\gamma, y_k)) & \text{if } \rho \in \mathcal{D}_2 \end{array} \right\}, S)} \text{Split}$$

## Example



## Splitting Processor

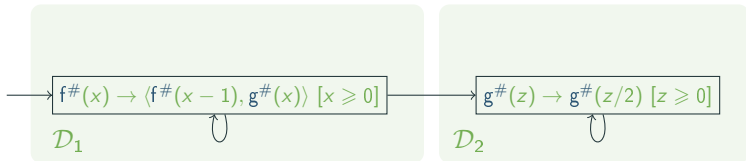
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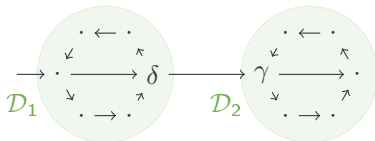
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## Example



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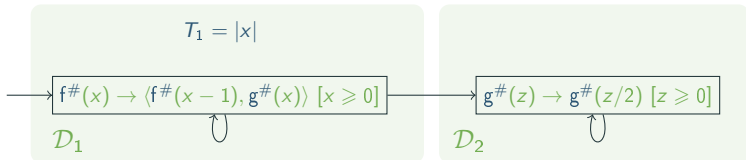
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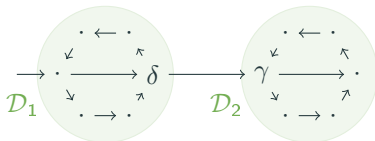
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## Example



## Splitting Processor

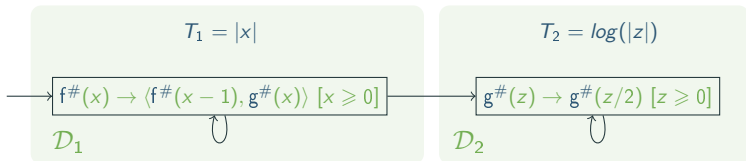
Let  $P = (t_0, \mathcal{D}, \mathcal{R})$  have dependency graph of shape



Then the following processor is sound, where  $\gamma: \ell \rightarrow r$  [ $\psi$ ]

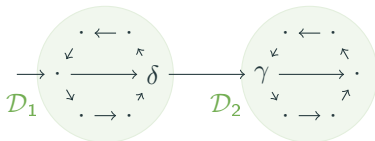
$$\frac{\vdash P: (T, S) \quad \vdash (t_0, \mathcal{D}_1, \mathcal{R}): (T_1, S_1) \quad \vdash (\ell, \mathcal{D}_2, \mathcal{R}): (T_2, S_2)}{\vdash P: (\lambda \rho. \left\{ \begin{array}{ll} T_1(\rho) & \text{if } \rho \in \mathcal{D}_1 \\ T(\delta) \cdot T_2(\rho)(S(\gamma, y_1), \dots, S(\gamma, y_k)) & \text{if } \rho \in \mathcal{D}_2 \end{array} \right\}, S)} \text{Split}$$

## Example



## Splitting Processor

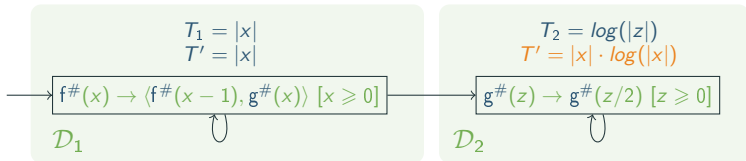
Let  $P = (t_0, \mathcal{D}, \mathcal{R})$  have dependency graph of shape



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## Recurrence Processor

For complexity problem  $P = (f(\vec{x}), \mathcal{D}, \mathcal{R})$  of form



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## Recurrence Processor

form can be generalized

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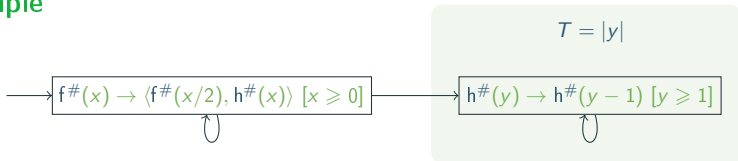


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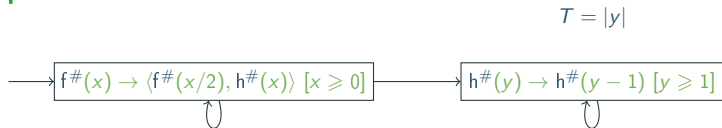


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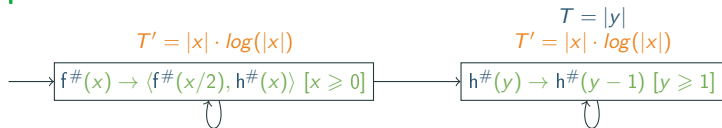


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- ▶ new module of complexity tool T<sub>C</sub>T: `tct-lctrs`

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- ▶ **ITS benchmarks**: optimal, sublinear bounds for several problems where other tools only yield polynomials



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- ▶ first **runtime complexity framework** for LCTRSs
- ▶ advance time/size bound approach by Brockschmidt *et al*, combine with complexity framework by Avanzini *et al*, extend to LCTRSs
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### theory:

- ▶ **more processors**: knowledge propagation, narrowing, ...
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### applications:

- ▶ non-deterministic (constraint) logic programs
- ▶ evaluation: ITS benchmarks (+ logic programs, SV-COMP)
- ▶ derivational complexity, e.g. for compiler simplification systems